MEMORANDUM

TO: TX-0 Computer Users

FROM: E. W. Pughe, Jr.

SUBJECT: NEW ORDERS FOR TX-0

DATE: 12 May 1959

Effective immediately the TX-0 Computer has one new order and two new operate class commands.

The new order is an unconditional transfer called "tra". Its code is 1010 and occupies the first four bits (0-1-2-3) of an instruction. The operation time is six microseconds.

The two new operate class commands are an "inclusive or" & "and". The code 0011 is the "inclusive or" and the code 0111 is the "and", for bits 9, 10, 11, and 15, respectively. The functions are performed between each bit of the accumulator and the corresponding bit of the live register with the answer appearing in the live register. The table below defines the functions "inclusive or" & "and".

ACC_N	LR_N	Inclusive Or	And
0	0	0	0
0	1	1	0
1	0	1	0
1	1	1	1

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