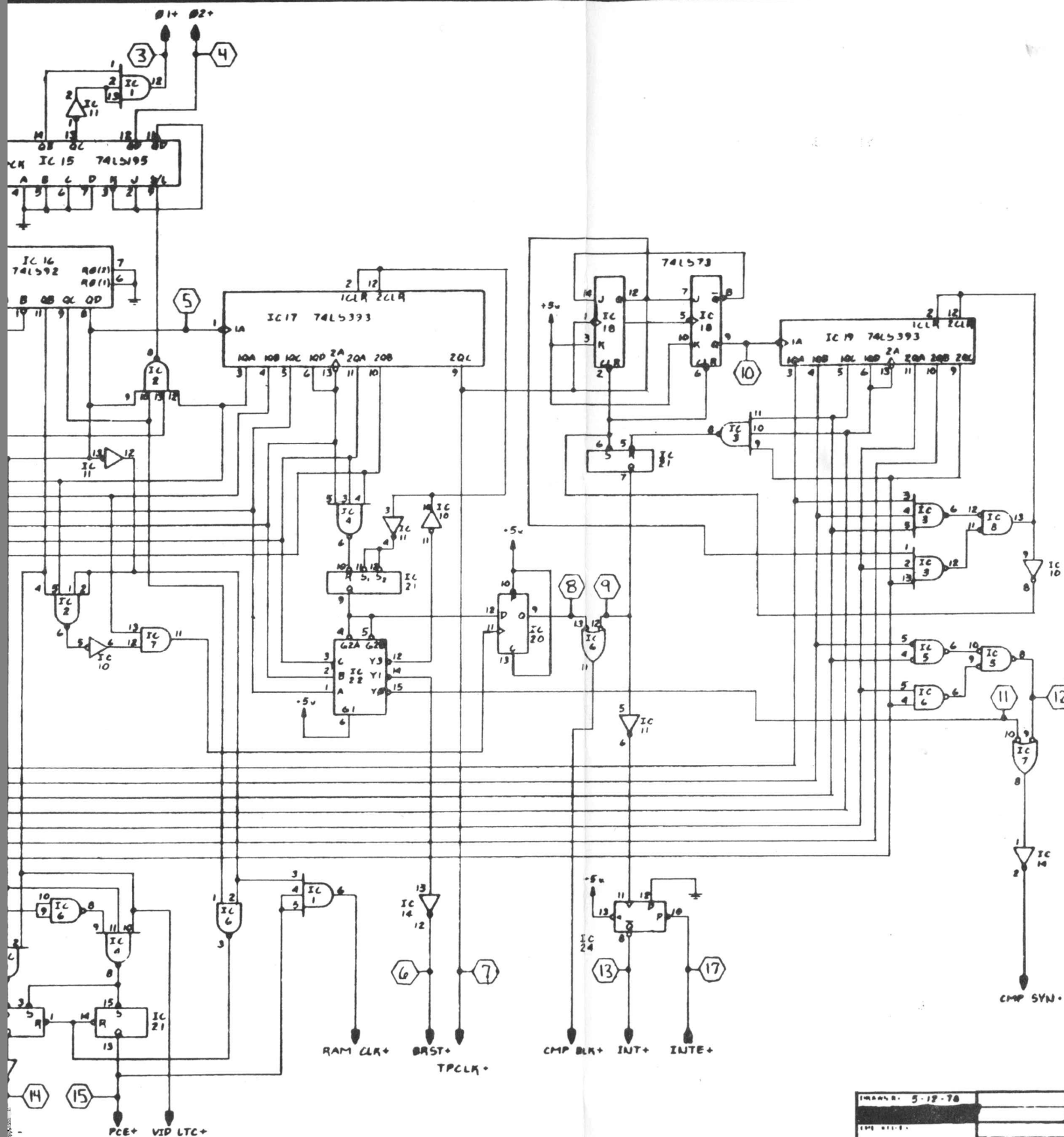


REVISIONS			
178	DESCRIPTION	DATE	APPROV
	REL FOR PROE		

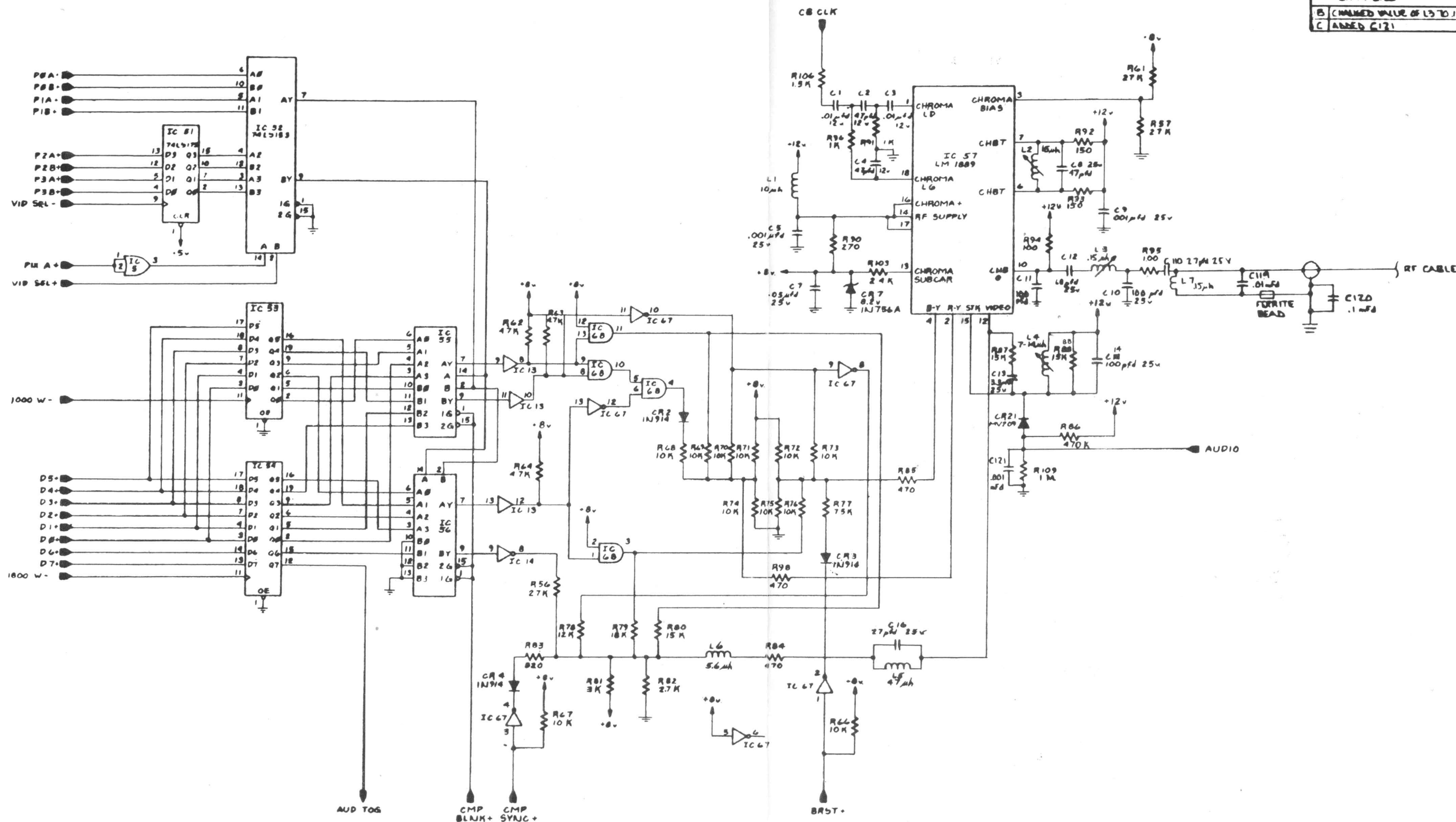


1.323 45

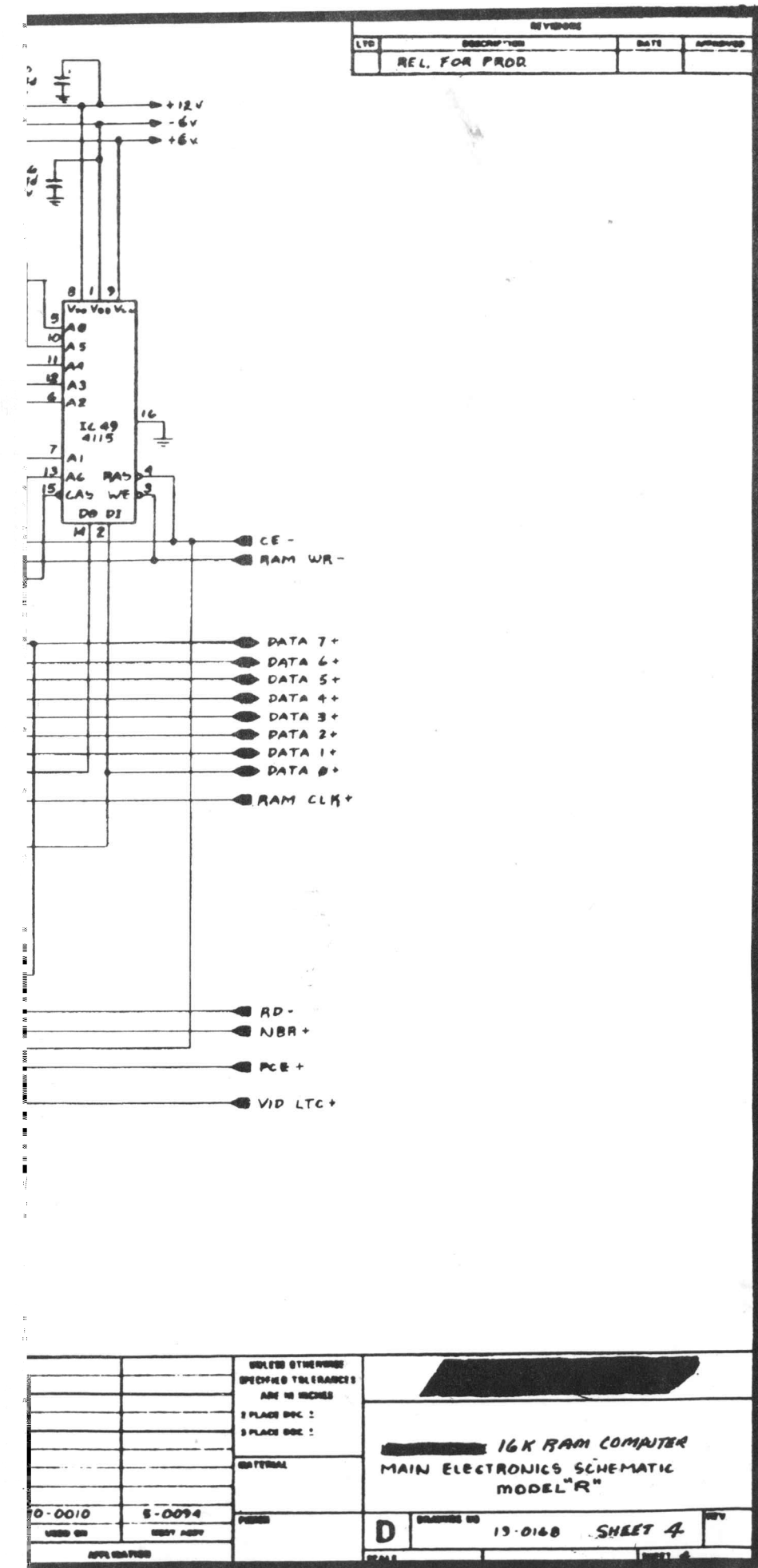
MEASUREMENT POINTS

ISSUANCE: 5-18-78			USE THE STRUCTURE SPECIFIED FOR PLACES AND NO OTHERS	
EPI TITLE:			1 PLACE BOX ? 2 PLACE BOX ?	
APPROVAL:			INTERNAL	16K RAM COMPUTER MAIN ELECTRONICS SCHEMATIC MODEL "R"
	10-0010	5-0094	POWER	D DRAWING NO 13 0168
	USED ON	REPT USED		SHEET 2
	APPROVED		SCALE	SHEET 2

REVISIONS			
1. NO	DESCRIPTION	DATE	APPROVED
	REL FOR PROD		
A	CHANGED C6 TO C11 TO 100 PPS + ADDED C119 + C120	B-24-78	
B	CHANGED VALUE OF L3 TO 15	B-26-78	
C	ADDED C121	9-1-78	



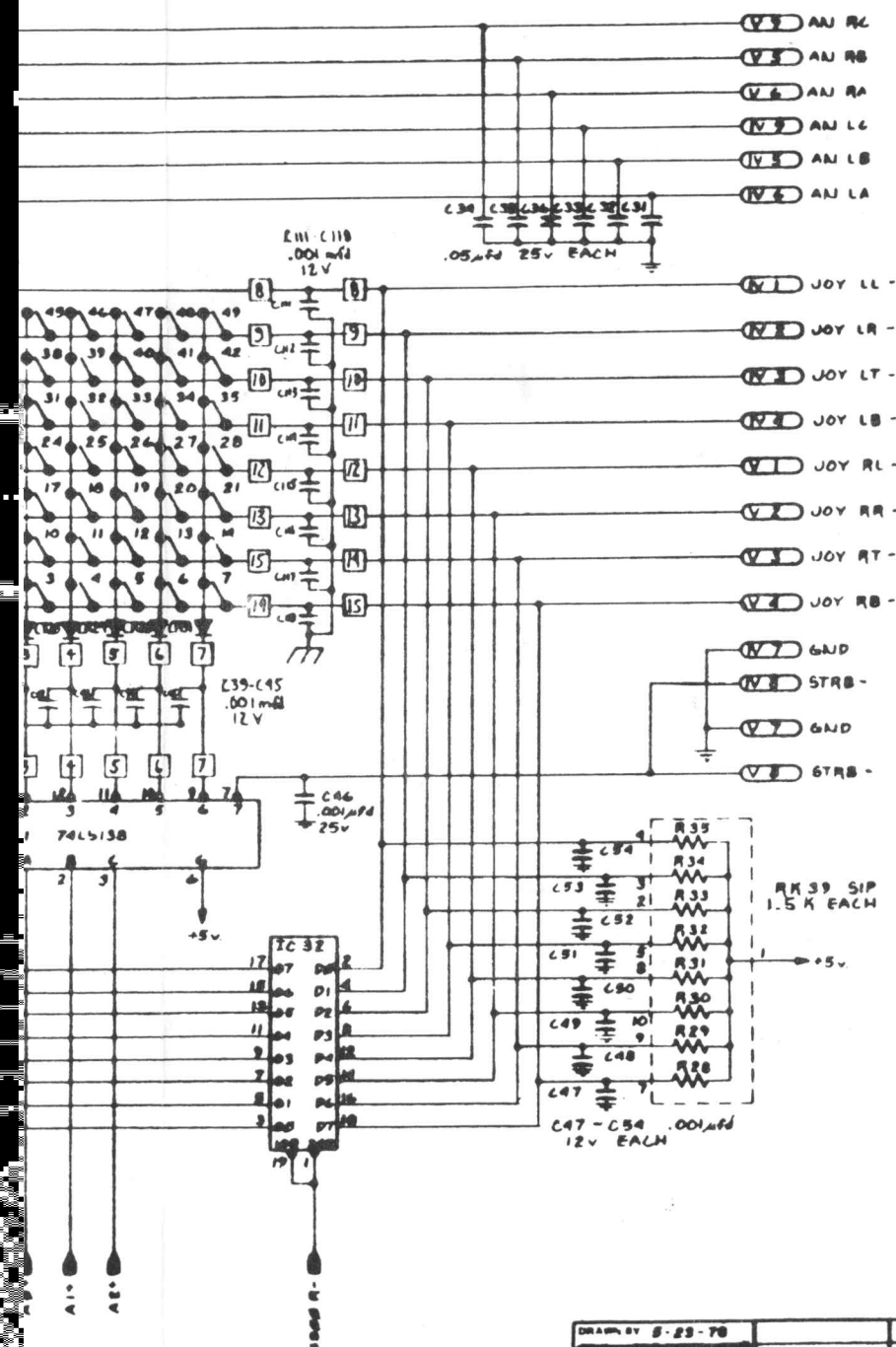
DRAWING BY	5-17-78			USE THE STANDARD SPECIFIED TOLERANCES ARE IN DECIMALS					
CHECKED BY				2 PLACE DEC : 3 PLACE DEC :					
APPROVED				NATIONAL	MAIN ELECTRONICS SCHEMATIC MODEL "R"				
	10-0010	5-0094		PAGE	D	PROJECT NO.	13-0168	SHEET 3	REV C
	USED OR	NEW COPY							
	APPLICATION								
				SCALE				SHEET 3	



LTD	DESCRIPTION	DATE	APPROVED
	REL. FOR PROD.		

UNLESS OTHERWISE SPECIFIED TOLERANCES ARE AS SHOWN		16K RAM COMPUTER	
3 PLACE DEC :		MAIN ELECTRONICS SCHEMATIC	
3 PLACE DEC :		MODEL "R"	
D-0010		D	
S-0094		19-0168	
USED ON		SHEET 4	
APPROVED		SCALE	

REVISION			
LTG	DESCRIPTION	DATE	APPROVED
1	REL FOR PROR	7 18 78	5-5



GND ON MAIN BOARD
 GND ON INTERCONNECT BOARD

DRAWN BY 5-23-78		UNLESS OTHERWISE SPECIFIED TOLERANCES ARE IN INCHES	
CHECKED BY		3 PLAS DEC 1	
APPROVED		3 PLAS DEC 2	
		EXTERNAL	
10-0010		5-0094	
REVISION		REVISION	
13-016A		SHEET 5	

