## INTRODUCTION TO CRYSTAL COMPONENTS

## GENERAL

Quartz crystals are electro mechanical resonators which are typically used in precision frequency applications such as system clocks, timing circuits, frequency control systems, and local oscillators for data communications systems. To meet frequency control needs, the user has the option of choosing a discrete crystal, with which an oscillator can be built with additional discrete components, or a hybrid crystal oscillator which provides the complete oscillator function in one microminiature package. (See Figure 7-1.)

Discrete crystals are available from 1 kHz to 92 MHz in various package sizes. Over 250 crystal part numbers exist, with up to 100 of them active (consult the listing in Section 7.4). Oscillators built from discrete crystals take up the space of about one $2 \times 4$ card and use upwards of 20 discrete components.

Hybrid crystal oscillators are available from 900 kHz to 70 MHz with approximately eighty-five released part numbers. The packages are a eight leaded T0-8 can which is 0.650 " in diameter and a Dip, 14 pin, 100 mil package. The oscillators require only a 5 Vdc supply and are completely TTL compatible, capable of driving up to 5 TTL loads. ECL compatible oscillators are also available.

The hybrid oscillator offers many advantages over discrete designs. They include:

1. Size - The hybrid oscillator occupies less than $1 / 20$ the floor space of discrete design.
2. Reliability - The failure rate of the complete hybrid oscillator is $1 / 2$ that of the quartz crystal alone, to which one must add the failure rates of up to twenty additional components and their interconnections.
3. Performance - The specified oscillator performance is exactly what the user will see in the application. With the discrete oscillator, the performance is only as good as the oscillator design.
4. Design Ease - No design is required with the oscillator.

Advantages of the hybrid crystal oscillator include:

1. Wide frequency range ( 900 kHz to 70 MHz ).
2. Customized to any logic family or power supply requirement.
3. Cost - The discrete oscillator is generally thought to be a lower cost than the oscillator. However, the oscillator cost is a total cost whereas the cost of a discrete design is complicated by fluctuating procurement costs, component cost, labor costs, and burden rates associated with oscillator manufacturing. The discrete oscillator may be cheaper, but not by a large amount.

## PASSIVE COMPONENTS MANUAL

CRYSTALS


Figure 7-1. Discrete Crystal Product Types Discussed in this Section

## QUARTZ CRYSTALS (DISCRETE)

## DESCRIPTION

A quartz crystal resonator consists of a finely machined and polished piece of crystalline quartz with two precious metal electrodes (usually gold or silver). The assembly is usually placed in a hermetically sealed metal can with two active leads. An electric potential placed across these leads causes a mechanical deformation of the quartz due to the piezoelectric effect. When subjected to an dc potential, the piezoelectric effect causes the quartz to physically vibrate in its mount. At certain frequencies, determined by the physical dimension and the mass of the quartz, standing waves, similar to acoustic waves, are set up in the quartz blank. At these resonant frequencies, the electrical impedance becomes very low. At all other frequencies, the impedance is ideally very high.

The quartz resonator is often represented as a bandpass filter as shown in Figure 7-2. The component values of the equivalent circuit are related to the physical parameters of the quartz blank as follows:

> Lm - motional inductance
> Cm - motional capacitance
> Rs - series resistance at resonant frequency
> Co - shunt capacitance of holder.

The exact relationships are quite complex and empirically derived.
The frequency performance of a quartz resonator is illustrated by Figure 7-3. Below resonance, the motional capacitance dominates the reactance. At series resonance (fs), Cm , and Lm resonate to give zero reactance. The device impedance is the series resistance of the resonator with 0 degrees phase shift. Above fs, the motional inductance dominates until parallel resonance with Co is reached. At this point, impedance is at a maximum and the phase shifts rapidly from $+90^{\circ}$ to $-90^{\circ}$. This point is termed the anti-resonant frequency fs and $f a$ is inversely proportional to the crystal $Q$. With few exceptions, crystal performance is specified around its series resonant point. Most oscillators are designed to have the crystal operate with a slightly positive reactance (See Figure 7-3, point X ).

The piezoelectric properties of quartz are anisotropic, that is, the effect of applying a voltage will cause an extension or a contraction in a different direction depending on the relative orientation of the crystallographic axis. There is, however, some symmetry. The anisotropy of quartz allows resonators to be designed over a wide range of frequencies ( 900 kHz to 70 MHz ) by changing the angle of the cut. For example, the flexural mode (see Figure 7-4) is used at low frequencies. The frequency determining dimension is length. This mode is relatively insensitive to thickness (except as it relates to mass). A piece of quartz several inches long would be used to provide say a 5 kHz resonator for example. To use the same cut at high frequencies (above 100 kHz ) would require extremely short blanks to contain the standing wave. Instead, alternative crystal cuts are used to excite different vibration directions. Figure 7-5 shows the approximate frequency ranges for which the various crystal cuts are useful.


Figure 7-2. Crystal Resonator Equivalent Circuit and Schematic


Figure 7-3. Frequency Performance of Crystal Resonator

## PASSIVE COMPONENTS MANUAL



Figure 7-5. Frequency Range Versus Vibration Mode, Package Size, and Cut

The following terms and definitions are useful in understanding quartz resonator performance.

Crystal Blank - the basic crystal slice which has been machined and polished; before the electrodes have been deposited.

Crystal Cut - a term which denotes the angle of the crystal blank with respect to the crystallographic axis.

Series Resonant Frequency (fs) - the frequency at which the motional capacitive reactance equals the motional inductive reactance; the series resonant impedance is at a minimum and the phase change is zero.

Series Resonant Impedance (Rs) - the effective resistance of a crystal operating at its series resonant frequency and specified drive level.

Anti-Resonant Frequency (fa) (parallel resonance) - the frequency at which the total capacitive reactance and the motional inductive reactance are in parallel resonance; the impedance is at a maximum.

Quality Factor (Q) - ratio of stored energy to dissipated energy; the sharpness of the peak at resonance; the slope of the phase versus frequency plot in the area of fs (See Figure 7-3).

Drive Level - the power dissipated by the crystal at series resonant frequency.
Spurious Modes - unwanted oscillations near the resonant frequency but slightly higher; caused by imperfections in the crystal structure, surface defects, mounting effects and edge effects.

## AVAILABLE TYPES

Table 7-1 summarizes the range of crystals currently used within IBM and their performance parameters. It represents the practical performance limits of the crystal industry today. Certain parameter performances can be improved upon in special cases (notably frequency tolerance of aging). The AT-cut, by far, represents the most commonly used crystal family.

Figure 7-5 shows the relationship among frequency, crystal cut, case and vibration mode. Figure 7-6 shows the physical outlines of the four crystal cans currently in use.

Figure 7-7A and 7-7B illustrate two typical mounting techniques needed to accommodate the various size crystal blanks.

|  | cut | Freq Range | Resistance Range （ohms） | $\underset{\text { Remp }}{\text { Range }}$ | Freq． Tol． | $25{ }^{\circ} \mathrm{C}$ Toi． | Q | $\begin{gathered} \text { Aging } \\ \left(85^{\circ} \mathrm{C}\right) \end{gathered}$ $\begin{gathered} (850 \mathrm{C}) \\ \substack{\text { PPM }} \end{gathered}$ | $\begin{aligned} & \hline \text { Drive } \\ & \text { Level } \\ & \text { (Max) } \end{aligned}$ | $\begin{gathered} \text { Motional } \\ \mathrm{L} \\ \text { (Henries) } \end{gathered}$ | $\begin{gathered} \text { Motional } \\ \text { C } \\ \text { (uuf) } \end{gathered}$ | $\begin{gathered} \text { Shunt } \\ \text { (uuf) } \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| J | Special | 2K－14KHz | 10к－200k | $-55^{\circ} \mathrm{C}$ to $90^{\circ} \mathrm{C}$ | $\pm .028$ | $\pm .00758$ | S－50K | ＜ 40 | 100uw | －250K | ．01－． 15 | 5－30 |
|  | Standard | $2 \mathrm{~K}-8.5 \mathrm{KHz}$ | 20к－150к | $+10^{\circ} \mathrm{C}$ to $600^{\circ} \mathrm{C}$ $+10^{\circ} \mathrm{C}$ to $60^{\circ} \mathrm{C}$ | ＝$=0.018$ | $\pm .018$ | － |  |  |  |  |  |
| XY | Special Special | 2K－50кHz | 20k－50K | $-55^{\circ} \mathrm{C}$ to $90^{\circ} \mathrm{C}$ | $\pm .028$ | $\pm .00758$ | 25－150 | $< \pm 30$ | 100uw | 00k | ．003－．035 | 1－25 |
|  | （tandard | 8． $5 \mathrm{~K}-16 \mathrm{KHz}$ | 10k－75K | $+10^{\circ} \mathrm{C}$ to $600^{\circ} \mathrm{C}$ $+10^{\circ} \mathrm{C}$ to $60^{\circ} \mathrm{C}$ | $\pm$$\pm .018$ <br> $\pm .0258$ | $\pm .018$ | －－－ |  |  | －－－ | －－－ |  |
| nt | Special | $14 \mathrm{~K}-150 \mathrm{KHz}$ | $8 \mathrm{k}-20 \mathrm{~K}$ | $-55^{\circ} \mathrm{C}$ to $90^{\circ} \mathrm{C}$ | ＝．0158 | ． 0.0058 | 30к－50k | $< \pm 20$ | 100uw | 250－ | ．002－． 03 | 3－15 |
|  | （tandard | 16K | 10k－75k | ＋100 $+=0$ |  | $\pm .018$ | －－－ |  |  | －－－ | －－－ | －－ |
| $5^{\circ} \mathrm{x}$ | Special | $90 \mathrm{~K}-300 \mathrm{KHz}$ | 1．5K－3k | $-55^{\circ} \mathrm{C}$ to $900^{\circ} \mathrm{C}$ | ＝．028 | $\pm .0028$ | 15－50K | ＜$\pm 10$ | 2.0 m | 30－100 | ．005－． 05 | ． |
|  | standard | $85 \mathrm{~K}-210 \mathrm{KHz}$ | ${ }^{800-3.5 \mathrm{~K}}$ | $+10^{\circ} \mathrm{C}$ to $60^{\circ} \mathrm{C}$ | －018 | $\pm .0058$ | －－－ |  |  | －－－ | －－－ |  |
| dт | $\mathrm{Sp}_{\mathrm{S}}$ | 200K－500khz | 800－4к | $-55^{\circ} \mathrm{C}$ to $900^{\circ} \mathrm{C}$ | $\pm .018$ | $\pm .00088$ | 20－50K | ＜$\pm 10$ | 2． 0 mw | 20－40 | ．003－． 03 | 1－10 |
|  | ¢ | 210K－400kHz | ${ }^{800-3.5 K}$ | $+10^{\circ} \mathrm{C}$ to $600^{\circ} \mathrm{C}$ $+10^{\circ} \mathrm{C}$ to $60^{\circ} \mathrm{C}$ | $\pm$ | $\pm .0058$ | －－－ |  |  |  | －－ |  |
| SL | S | 200k－19H2 | 500－3K | $-55^{\circ} \mathrm{C}$ to $90^{\circ} \mathrm{C}$ | $\pm .0098$ | ＝．00088 | 25－55k | $< \pm 10$ | 2.0 mw | 7－20 | ．004－．03 | 1－10 |
|  |  | 400x－960\％： | $880-3.5 \mathrm{k}$ | Fiter | ＝0028 | $=.0058$ |  |  |  | －－ |  |  |
| ${ }^{\text {at }}$ |  | $500 \mathrm{~K}-150 \mathrm{MHz}$ | 3\％－1．5k | $-5500=9300$ |  | $=.00058$ | 30－500k | $< \pm 10$ | 5．Omw | ．01－20 | ．001－． |  |
|  | Special Standard |  |  |  | $=00018$ $=0.058$ | $\pm .0028$ | ここ |  |  | こ－－ | －－－ | －こ： |
|  |  | 1．3M－2．0MHz | 50－300 |  |  |  | －－－ |  |  |  |  |  |
|  |  |  | － |  |  |  |  |  |  |  |  |  |
|  |  | M－8．0MHz | 10－60 |  |  |  |  |  |  |  |  |  |

Table 7－1．Electrical Parameter Capabilities of Crystals

PASSIVE COMPONENTS MANUAL


Figure 7-6. Crystal Cases (Part 1 of 3 )

## PASSIVE COMPONENTS MANUAL



Figure 7-6. Crystal Cases (Part 2 of 3)

## PASSIVE COMPONENTS MANUAL



Dimensions In Inches

Figure 7-6. Crystal Cases (Part 3 of 3)


Figure 7-7A. Typical Construction (Non-AT Cut)


Figure 7-7B. Typical Construction (AT Cut)

## PERFORMANCE CHARACTERISTICS

In this section, important resonator performance parameters will be described as they relate to design and processing techniques. Specification of these parameters is required for all crystal resonators. Table 7-1 summarizes the capabilities of the various crystal cuts.

Frequency Performance - the room temperature accuracy capabilities of the various crystal cuts range from $\pm 100$ PPM which is standard for AT cut crystals to $\pm 10$ PPM obtainable at a higher cost.

Frequency versus Temperature - the AT cut has by far the best frequency versus temperature performance. Figure 7-8 shows the theoretical $f$ versus $T$ performance for AT cut crystals as a function of the crystallographic angle of the crystal blank. Each curve represents a different angle with respect to the ide-

## PASSIVE COMPONENTS MANUAL

al AT cut angle of $35^{\circ} 21^{\prime}$ away from the crystallographic $Z$-axis. Note the flat portion of the curve in the usable temperature range of $0^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}$. This in large part accounts for the popularity of the AT cut. Figure 7-9 shows the performance of several other important crystal cuts.


Figure 7-8. Frequency versus Temperature for an AT Cut Crystal (Theoretical)



Figure 7-9. Typical Percent Change in Frequency versus Temperature, Non-AT-Cut

Impedance - crystal impedance is related to the amount of energy required to sustain the mechanical vibrations in the quartz blank. The size of the electrode, the crystal finish, mounting resistance, and the atmosphere in the crystal can, all affect the impedance (or "activity") of a crystal.

The electrode size is most often used to adjust the impedance of a given crystal unit. Increasing the size of the electrode activates more of the quartz and generally results in a lower impedance. However, this also decreases the resistance of spurious modes, which increases the probability of "hopping" to an unwanted frequency.

Spurious Modes - "Spurs" are unwanted dips in the impedance of the crystal at frequencies close to, but above, the series resonant frequency. They result from imperfections in the crystal finish (for example, non-parallel surfaces) and edge effects. Spurs are minimized by using smaller electrodes and by contouring one or both crystal surfaces to restrict the pieyoelectric activity to the center of the blank.

Harmonics - most crystal cuts are useful only at the fundamental frequency. The AT cut, will resonate at any frequency for which the thickness is an integral number of wavelengths such that a standing acoustic wave is set up. This happens at the fundamental and at all odd overtones. Oscillator circuits will tend to operate at the lowest frequency at which there is sufficient gain for oscillation. The useful frequency ranges for AT cut crystals are:

```
fundamental: 550 kHz to 20 MHz
3rd overtone: }10\textrm{MHz}\mathrm{ to }60\textrm{MHz
5th overtone: 45 MHz to 125 MHz
```

The lower frequency limit is due to the large thickness required and the increasing exposure to unwanted modes. The upper limit is due to the difficulty in processing extremely think and fragile blanks.

Overtone crystals generally have a higher impedance but a higher $Q$ than comparable fundamental. They are also more susceptible to spurs. When designing an oscillator with an overtone crystal, a filter must be used to attenuate the fundamental.

Some parameters, less often specified, are:
Q - the quality factor is a measure of the selectivity of the crystal as seen in the phase versus frequency plot of Figure 7-3. The higher $Q$ crystal will have a sharper slope. $Q$ is related to the crystal finish and the thickness of the crystal. In general, $Q$ is inversely proportional to frequency.

Co - shunt capacitance is the capacitance of the crystal measured at some frequency below the fundamental series resonance. It is related to the size of the electrodes and thickness of the crystal.

## EOL/Aging

The principal affect of aging in quartz resonators, is a gradual shift in frequency. This results from the relaxations of the strains induced by the machining, plating and mounting of the quartz blank. The rate of aging is at a maximum early in life and generally, but not always, results in decrease in frequency. The rate decreases to a low level that causes shifts in either direction. Short term aging can last up to a year depending on the operating conditions. Long term aging is usually an order of magnitude less than short term aging. Table 7-2 summarizes crystal aging rates for various cuts.

The worst case EOL frequency drift for an AT cut crystal would be the sum of the frequency shift throughout the operating temperature range ( $\pm 50 \mathrm{PPM}$ ) and the five year aging ( $\pm 36$ PPM) or $\pm 86$ PPM. Typical EOL performance would be closer to $\pm 40$ PPM.

The supported failure rate for quartz crystals is $0.02 \%$ per 1 k hours with a useful life of 12 years.

Table 7-2. Quartz Crystal Aging Rates by Cut

| Cut | 1 year Maximum | Typical | 5 year (EOL) |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  | Maximum | Typical |
| AT | 10 | 5 | 36 | 15 |
| $\begin{aligned} & \mathrm{XY}, \mathrm{NT}, \\ & 5^{\circ} \mathrm{X}, \mathrm{SL} \end{aligned}$ | 13 | 6 | 47 | 20 |
| DT | 25 | 12 | 90 | 38 |
| JT | 30 | 14 | 108 | 45 |

## DESIGN/APPLICATION CONSIDERATION

Since a quartz resonator is an electro-mechanical vibrator, it is quite sensitive to mechanical and thermal shock. Care should be taken to hand insert all crystals but wave soldering is permissable.

Crystals are quite sensitive to drive level. Increased drive level can greatly accelerate aging. A maximum drive level is specified for each crystal and determines at what level the crystals are measured. It is recommended that crystals be operated at as low a level as is practical.

To minimize the cost of a crystal, potential users should avoid over-specification in the following areas:

- Tighter frequency tolerance than needed.
- Minimum series impedance.
- Low aging requirements.
- Tight frequency versus temperature performance.
- Motional parameters (Capacitance, inductance).
- $\quad$ Q.
- Special packages.


## RELIABILITY CONSIDERATIONS

The SPQL for quartz crystals is 700 PPM. There is no ELAL for quartz crystals but crystal lifetime will be increased and failure rate decreased by reducing drive level and/or reducing temperature.

## SPECIFICATIONS

The following specifications apply to Quartz Crystals:

| Engineering Specifications | -890754 |
| :--- | ---: |
| Quality Specifications | -873565 |
| Glass to metal mechanical | 873589 |
| requirements | -873522 |
| Glass to metal seal <br> package requirements | -873523 |

DCS codes: 23751 - Solder Seal Package
23752 - Kold Weld Package
23759 - Special Devices

## CRYSTAL OSCILLATORS

## DESCRIPTION

The hybrid crystal oscillator provides precise frequency control and TTL compatibility in one miniature, hermetic package. The oscillator and output elements are provided by a thick film hybrid circuit. An AT-cut crystal, similar to those described in the previous crystal section, is suspended over the hybrid substrate by mounting posts. A 5 volt dc source and ground is all that is required for proper operation.

Two major elements are needed to synthesize an oscillator: an amplifier, and a positive feedback loop (see Figure 7-10A). To sustain oscillation, the Barkhausen criteria must be satisfied; that is, the amplifier gain times the transmission coefficient of the feedback loop must be greater than or equal to unity. In addition, the phase shift in the loop must be zero or some internal multiple of $360^{\circ}$. For efficient oscillator operation, the feedback loop should be a low impedance (high transmission coefficient) and should be reactive so as to offset any phase shift in the amplifier section. For stable oscillation, the feedback elements should have a high $Q$ so that, for a slight change in frequency, there is a very large change in impedance and phase angle of the feedback elements. No component or network behaves more suitably for this application that a quartz crystal resonator. Figure 7-10B illustrates a schematic for a typical positive reactance crystal resonator.


Barkhausen Criteria:

$$
\begin{aligned}
\alpha \beta & \geq 1 \\
\Delta \Phi & =n 360 \quad n=0,1,2, \ldots
\end{aligned}
$$

Figure 7-10A. Block Diagram of Oscillator Which Satisfies Barkhausen Criteria


Figure 7-10B. Typical Transistor Sine Wave Oscillator Schematic

The microminiature crystal oscillator provides the elements of a precision crystal-controlled oscillator plus a TTL logic interface in a hermetic package smaller in size than most discrete crystals. Figure 7-11 represents a discrete solid state oscillator, which could have a count of 20 or more discrete components, compared to a single hybrid oscillator. The oscillator has a diameter of 0.650 inches and a height of 0.350 inches.

Figure 7-12 depicts the hybrid substrate, the substrate with the crystal attached, and a completed unit. The substrate supports a thick film hybrid circuit containing an amplifier, voltage dividing networks, and an output buffer for TTL or a frequency divider, which is also TTL compatible. Conventional high performance screened and fired thick film resistor technology is employed with either sand or laser abrades. Ceramic chip capacitors are either reflow attached or epoxy bonded to the lands and conventional die attach and wire bond techniques are used on the semiconductor dice. The crystal blank is epoxy attached to leads suspending it above the hybrid substrate. The assembly is then hermetically sealed in an inert atmosphere. The hermetic environment which is required for the quartz crystal operation substantially improves the reliability expectations of the semiconductors, chip capacitors, and resistors which are conventionally plastic encapsulated.

Figure 7-13 shows the generalized manufacturing sequence.


Figure 7-11. Size Relationship Between Printed Circuit Board Layout and Hybrid Package


Figure 7-12. Sequence from Substrate to Final Package

$i$

Figure 7-13. Generalized Process Flow Chart for Hybrid Oscillator

## AVAILABLE TYPES

Crystal oscillators are currently available in two packages: an eight leaded TO-8 case and a 14 Pin DIP. Figures $7-14 \mathrm{~A}$ and $7-14 \mathrm{~B}$ show the physical outlines. Of the eight TO-8 leads, three of four are active depending on number of outputs. Three pins are active on the DIP package with one output active and all four active when a dual output is required.

Currently available frequencies in this package range from 900 kHz to 70 MHz . The upper frequency limit is determined by the difficulty of manufacturing fundamental mode AT cut crystals above 25 MHz , while the lower limit is determined by the maximum allowable size of the AT cut crystal and the number of frequency dividers that will fit in the TO-8 or DIP package. There are, of course, other crystals cuts which allow lower frequencies in even smaller packages, but they cannot match the performance and aging characteristics of the AT cut.

Multiple outputs are available on several part numbers to provide divided frequencies or complementary outputs.


Figure 7-14A. Physical Outline of the T0-8 Can


Figure 7-14B. Physical Outline of DIP Packaging

## PERFORMANCE CHARACTERISTICS

The frequency performance of the crystal oscillator is dominated by that of the crystal. The frequency tolerance of $\pm 25 \mathrm{PPM}$ at $25^{\circ} \mathrm{C}$ for the oscillator compares well to that of discrete crystals described in the previous section.

Frequency versus temperature performance is also determined by the basic crystal performance. Figure $7-15$ shows the actual performance of two representative devices over the operating range of $-10^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$. Both devices are well within the typical specification tolerance of $\pm 50$ PPM over the temperature range and indicate a different cutting angle. These samples represent devices from two different manufacturers. Product from the same manufacturer generally are more tightly distributed with respect to angle and therefore temperature performance.

Although the frequency performance is probably the most important oscillator specification, much more must be said about the digital output than simply that it is TTL compatible. Nominal values and tolerances are assigned to all the waveform parameters and a minimum load is specified. Figure 7-16 is an idealized TTL square wave showing the rise time, symmetry, and logic levels. Rise

## PASSIVE COMPONENTS MANUAL

times are typically 12 to 15 nsec maximum when measured from 0.5 V to 2.4 V on the leading edge. Symmetry is the percent ratio of the up time to the total cycle time as measured at the 1.5 V level, and can range from $\pm 15 \%$ to $\pm 5 \%$ of the nominal of $50 \%$. At higher frequencies (above 10 MHz ) it is more difficult to hold to tighter timing tolerances and it may be required that high speed Schottky devices be employed. It is required that the logic "1" voltage not fall below the 2.4 V least positive up level (LPUL) and that the logic "0" voltage never exceed the 0.5 V Most Positive Down Level (MPDL). These waveform specifications and voltage levels are compatible with those required to drive standard TTL devices available in the semiconductor industry today. They are, however, more explicit and somewhat more conservative than most performance specifications available from TTL suppliers.

Required test circuits and measurement procedures are explicitly specified to minimize correlation difficulties. The measurement circuit specified is equivalent to 10 TTL gates but users are instructed to use no more than a fan-out of 5 inches of their applications.

Dual outputs are available for special applications providing complementary (inverted waveform at the same frequency) and divided frequency (up to +16 ) outputs. The potential is there for other related outputs which could be exploited if the need arose.

Contrary to previous considerations related to frequency performance, the output waveforms are very sensitive to parametric changes of the hybrid components and to loading also; probably more so than from those of the crystal.

Table 7-3A. A Typical Parametric Ranges of TO-8 Oscillators

| Parameter | Values | Tolerances |
| :--- | :--- | :--- |
| Frequency | 250 kHz to 25 MHz | $\pm 0.0025 \%$ at $25^{\circ} \mathrm{C}$ |
| Temp. vs. frequency | $\pm 50 \mathrm{PPM}$ from $10^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}$ | Maximum |
| Rise time | 12 nsec to 15 nsec | Maximum |
| Fall time | 12 nsec to 15 nsec | Maximum |
| Symmetry | $50 \%$ (skewed waveforms |  |
| are possible if required) | $\pm 5 \%$ to $\pm 15 \%$ |  |
| Logic "1" | 2.4 volts | Minimum |
| Logic "0" | 0.5 volts | Maximum |

## PASSIVE COMPONENTS MANUAL

Table 7-3B. Typical Parametric Ranges of DIP Oscillators

| Parameter | Values | Tolerances |
| :--- | :--- | :--- |
| Frequency | 0.9 MHz to 70 MHz | $\pm 0.004 \%$ at $25^{\circ} \mathrm{C}$ |
| Temp. vs. frequency | $\pm 50 \mathrm{PPM}$ from $10^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}$ | Maximum |
| Rise time | 5 nsec to 18 nsec | Maximum |
| Fall time | 5 nsec to 18 nsec | Maximum |
| Symmetry | $50 \%$ | $\pm 5 \%$ to $\pm 15 \%$ |
| Logic " $1 "$ | 2.4 volts | Minimum |
| Logic " 0 " | 0.5 volts | Maximum |




Figure 7-16. TTL Waveform

A major factor contributing to the success of hybrids in general is their proven reliability advantage over comparable discrete designs. The hybrid oscillator is no exception. Besides the mechanical interconnection advantages of the thick film technology, the better part-to-part uniformity of the components in the hybrid design offers tighter control over the drive level of the crystal. The expected aging rate of quartz crystals is directly related to the drive level, all other things being equal. In addition, the integrated output stage, to a large degree, isolates the oscillator from the load. Both factors raise the performance and reliability expectations of the hybrid design.

For any oscillator to demonstrate aging rates similar to discrete crystals would be noteworthy, since the discrete crystals must be subjected to another level of assembly and associated tolerance accumulations and sampling errors before they become functional. Figure 7-17 represents how well the oscillators perform with respect to frequency over extended life. The graph shows the fractional change of frequency exhibited by a group of typical oscillators operating at full load at $85^{\circ} \mathrm{C}$ for up to 25,000 hours. After an initial decrease in frequency, the rate of aging decreases to a low level with most of the aging occurring in the first 30 days. This performance is typical of AT cut quartz crystals alone. The fact that the aging is negative and is decreasing in rate indicates that a mass sorption mechanism is at work on the crystal. Since the frequency of the crystal is determined by the mass, as well as by the dimensions of the crystal blank, contaminants (water vapor, outgassing) lower the frequency until an equilibrium is reached. After several thousand hours, the aging rate is at a very low level and shows neither positive nor negative tendencies.


Time (in Hours)

Figure 7-17. Oscillator Aging at $85^{\circ} \mathrm{C}$

As stated earlier, the frequency performance is assumed to be a measure primarily of the crystal performance. The waveform parameters of rise time and symmetry, however, which are a direct measure of the hybrid substrate performance, are similarly well behaved over extended life. The rise times and symmetries over the 25,000 hours typically change very little, on the order of 1 nsec , approaching the measurement system accuracy.

The overall failure rate profile could then qualitatively be described as consisting of an early failure mechanism and a long term wear out mechanism of the type normally associated with semiconductors. The profile resembles the classical "bathtub" failure rate curve. The early period is dominated by crystal performance as related to general quality and workmanship (crystal plating, contaminants, lead bonding, hermetic seal). Occurrences of failures of this type peak out relatively early in the component's life. A period of low, relatively stable, failure rates follows. This period is usually termed "useful life". A period of rising failure rates due to wear out can be expected to follow. The wear out mechanism is related to the chemical and mechanical wear out of the semiconductors. The passive thick film elements and the capacitors do not materially contribute to the failure rate.

Although the analysis to date is not rigorous enough to quantitatively determine the failure rate associated with each period, the hybrid oscillator is an extremely reliable component, particularly when compared to the discrete alternatives. The reliability advantage of the hybrid comes primarily from the reduced component count ( 1 versus $\sim 20$ ), and, therefore, the reduced number of interconnections, and fewer total process steps. There is only one encapsulation process, which is hermetic, required for the quartz whereas in a discrete design a separate encapsulation process with an associated failure rate is needed for each component. The reliability advantage of the hybrid over the discrete version should approach an order of magnitude.

EOL frequency performance is identical to that for AT-cut discrete crystal. Worst case EOL frequency change as a result of temperature and aging should not exceed $\pm 75$ PPM and would nominally be closer to $\pm 40$ PPM.

The supported failure rate for crystal oscillators $0.01 \% / 1 \mathrm{k}$ hours with a useful life of 12 years.

## DESIGN/APPLICATION CONSIDERATION

Crystal oscillators in the T0-5 package are very rugged. However, for the quartz crystal to operate properly, a delicate mounting design is necessary. As such, the device is sensitive to mechanical and thermal shock.

The output waveform is sensitive to power supply regulation and loading. Fan-out should be limited to 5 (the fewer, the better) and long lead lengths should be avoided. In all ways, the output should be treated as one would treat the output of any SN74XX gate.

Most of the oscillators draw about 20 mA to 50 mA of dc current at 5 volts. Temperature rise can be from $5^{\circ} \mathrm{C}$ to $10^{\circ} \mathrm{C}$ in still air. Short duration voltages of more than 7 volts may destroy the device.

Turn on time for most oscillators is less than 100 msec . For special applications, turn on times of less than 20 msec can be specified. It is recommended however that sufficient time ( $>200 \mathrm{msec}$ ) be allowed for the oscillator to start and demonstrate a stable output.

To minimize the cost of an oscillator, it is recommended that the following be avoided:

1. Overly tight frequency, or frequency versus temperature tolerance.
2. Symmetry tolerance less than $\pm 10 \%$.
3. Frequencies less than 8 MHz or greater than 18 MHz .
4. Dual outputs.
5. Any other non-standard performance parameter.

## RELIABILITY CONSIDERATION

The SPQL for crystal oscillator is 1000 PPM. No ELAL failure rate algorithm exists for crystal oscillators but lifetime can be increased and failure rate decreased by reducing the load, temperature, or the number of on/off cycles.

## PASSIVE COMPONENTS MANUAL

## SPECIFICATIONS

```
The following specifications apply to crystal oscillators:
Engineering Specification - 865799
Quality Specification - 866440
    873589
Glass-to-metal Seal
    Mechanical Requirements - 873522
Glass-to-metal Seal
    Package Requirements - 873523
DCS Codes: 23741 - TTL
    23742 - ECL
    23749 - Specials
```


## QUARTZ CRYSTALS (DISCRETE)

Component Data Bank - P/N Catalog
DCS CODE

23751





## CRYSTAL OSCILLATORS

Component Data Bank - P/N Catalog
DCS CODE

23741
23742

PG. 1 06/30/82 23:36 URO206 ${ }^{* * *}$ IBM INTERNAL USE ${ }^{*}$ ** COMPONENT DATA BANK INTERNAL USE ONLY CDB/OSC ALL/OSC TECH OSC/PARI SEQ/LH OSC/FREQ/HZI NO/LIMIT.
PART
PUMBER T FIRST O/P
U FREQUENCY
C HERTZ 1582849
1582994 1582994 1582996 1589258 4429964 E 1582718
8493422 8493243 8493514 8493201 8519749 4429664
8493814 $L$
$L$
$L$
$L$
$E$ 2396960 4430008 4430076 1589408 2397044 8493409 8519177 4430037 C 8493414
1582532 8519178 1582500 6833138 5616635 5615755 2410165 5615753 8519576 2397049
1582988 1582988 8519175 1582564
2397062 2397062
4430045 3519500 2396875 2396875
2397082 2397082
-5616004 2397050 4429947 C $10,000,000.00$ 5615371 E 10,752,000.00 1582531 E 11,200,000.00 4481464 A 11,664,000.00 2396788 E 11,796,000.00 SECOND ORP SYM-
FREQUENCY METRY
HERTZ FREQ. FREQ. TOL
25 DEG C C TOLOTO TECH- INPUT MAXIMUM CAN
FAM- NOLOGY VOLTAGE CAP.LD. SIZE
ILY $\begin{array}{rrr}.00 & \text { NO DATA } & \\ .00 & \text { NO DAATA } & \\ .00 & \text { NO DATAA } & \\ .00 & \text { NO DATA } & \\ .00 & \text { NO DAAAA } & \\ .00 & \text { NO DATA } & \\ 900,800.00 & & 10 \\ 900,800.00 & & 10 \\ 1,302,500.00 & & 10 \\ 2,097,152.00 & & 10 \\ 2,500,000.00 & & 15 \\ 2,621,440.00 & & 15 \\ 3,000,000.00 & & 10 \\ 3,579,545.00 & \text { COMP } & \end{array}$ 4,000,000.00
$4,000,000.00$ 1,000,000 $4,915,200.00$
$4,915,200.00$
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. $\quad .006 \%$


| PART NUMBER | T FIRST O/P <br> U FREQUENCY <br> C HERTZ | SECOND O/P FREQUENCY HERTZ | $\begin{gathered} \text { SYM- } \\ \text { METRY } \\ \% \end{gathered}$ | $\begin{aligned} & \text { FREQ. } \\ & \text { TOL } \% \text { DEG C } \end{aligned}$ | $\begin{aligned} & \text { FREQ. } \\ & \text { TOL } \\ & \text { OTO70 } \end{aligned}$ | FAMILY | TECHNOLOGY | INPUT voltage $+-10 \%$ | $\begin{aligned} & \text { MAXIMUM } \\ & \text { CAP.LD. } \\ & \text { P.F. } \end{aligned}$ | $\begin{aligned} & \text { CAN } \\ & \text { SIZE } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 4429707 | C 12,000,000.00 |  | 10.00 | . $004 \%$ | . $006 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 1582989 | E 12,500,000.00 |  | 10.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 8519176 | C 12,800,000.00 |  | 10.00 | . $01 \%$ | . $01 \%$ |  | TTL | 5.00 | . 00 | DIP |
| 1589283 | E 13,824,000.00 |  | 10.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 2410083 | E 14,000,000.00 |  | 15.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 8493782 | C 14,152,300.00 |  | 10.00 | . $008 \%$ | . $01 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 1589284 | E 14,688,000.00 |  | 10.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | TO-8 |
| 8493554 | C 14,745,600.00 |  | 10.00 | . $004 \%$ | . $006 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 2410055 | E 15,091,200.00 |  | 15.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 2397020 | E 15,552,000.00 |  | 10.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 4481935 | E 15,552,000.00 |  | 5.00 | . $004 \%$ | . $006 \%$ |  | TTL | 5.00 | 35.00 | T0-8 |
| 2410196 | E 16,000,000.00 |  | 15.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 8279298 | C 16,000,000.00 |  | 15.00 | . $035 \%$ | . $045 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 8493355 | C 16,588,800.00 |  | 10.00 | . $01 \%$ | . $01 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 8493500 | C 17,000,000.00 |  | 15.00 | . $004 \%$ | . $006 \%$ |  | TTL | 5.00 | 35.00 | T0-8 |
| 8519702 | C 17,241,400.00 |  | 10.00 | . $35 \%$ | . $40 \%$ |  | TTL | 5.00 | . 00 | DIP |
| 4429685 | E 18,000,000.00 |  | 10.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 8519575 | C 18,000,000.00 |  | 15.00 | . $035 \%$ | . $045 \%$ |  | TTL | 5.00 | . 00 | DIP |
| 5617084 | E 18,181,800.00 | 0 | 10.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 8493413 | C 18,200,000.00 |  | 15.00 | . $035 \%$ | . $045 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 4429943 | C 18,432,000.00 |  | 15.00 | . $035 \%$ | . $045 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 5616003 | E 19,660,800.00 |  | 10.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 4429906 | C 19,968,000.00 |  | 5.00 | . $004 \%$ | . $006 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 1582620 | E 20,000,000.00 |  | 15.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | TO-8 |
| 8493207 | c 20,000,000.00 |  | 5.00 | . $008 \%$ | . $010 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 8493204 | C 20,007,000.00 |  | 5.00 | . $0040 \%$ | . $0060 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 8493423 | C 20,480,000.00 |  | 10.00 | . $01 \%$ | . $01 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 4429937 | C 20,833,000.00 |  | 10.00 | . $35 \%$ | . $4 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 5615989 | E 21,504,000.00 |  | 15.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 4430046 | C 21,582,700.00 |  | 5.00 | . $004 \%$ | . $006 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 4481730 | C 21,792,000.00 |  | 5.00 | . $008 \%$ | . $01 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 2397042 | E 24,000,000.00 |  | 15.00 | . $004 \%$ | . $006 \%$ | 3 | TTL | 5.00 | 35.00 | T0-8 |
| 8493356 | C 24,000,000.00 |  | 5.00 | . $1 \%$ | . $1 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 8272248 | C 25,000,000.00 |  | 10.00 | . $1 \%$ | . $1 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 4430021 | E 26,666,600.00 |  | 10.00 | . $0025 \%$ | . $0025 \%$ | 8 | TTL | 5.00 | 35.00 | T0-8 |
| 8493533 | C 28,571,400.00 |  | 5.00 | . $004 \%$ | . $006 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 4481795 | C 32,000,000.00 |  | 10.00 | . $004 \%$ | . $006 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 6833170 | E 32,000,000.00 |  | 10.00 | . $004 \%$ | . $006 \%$ | 8 | TTL | 5.00 | 35.00 | TO-8 |
| 5833178 | E 35,714,300.00 |  | 10.00 | . $01 \%$ | . $02 \%$ |  | ECL | 4.25 | . 00 | T0-8 |
| 5616807 | E 36,363,600.00 |  | 10.00 | . $004 \%$ | . $006 \%$ | 8 | TTL | 5.00 | 35.00 | T0-8 |
| 6833176 | E 37,037,000.00 |  | 10.00 | . $01 \%$ | . $02 \%$ | 6 | ECL | 4.25 | 5.00 | T0-8 |
| 6833174 | E 38,461,500.00 |  | 15.00 | . $01 \%$ | . $02 \%$ |  | ECL | 4.25 | 35.00 | T0-8 |
| 4429905 | C $40,000,000.00$ |  | 10.00 | . $004 \%$ | . $006 \%$ | 10 | TTL | 5.00 | 35.00 | DIP |
| 5615754 | E 40,000,000.00 |  | 5.00 | . $01 \%$ | . 02\% |  | ECL | 4.25 | 5.00 | TO-8 |
| 8519559 | C $40,000,000.00$ |  | .00 | . $004 \%$ |  |  | ECL | 5.00 | 5.00 | DIP |
| 8493537 | E $40,816,300.00$ |  | . 00 | . $01 \%$ |  |  | ECL | 4.25 | . 00 | T0-8 |
| 8493846 | C 48,000,000.00 |  | 5.00 | . $004 \%$ | . $006 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 4481264 | C 50,000,000.00 |  | 10.00 | . $1 \%$ | . $1 \%$ |  | TTL | 5.00 | 35.00 | DIP |
| 4430083 | C 58,982,400.00 |  | 10.00 | . $005 \%$ | . $01 \%$ |  | ECL | 5.00 | 35.00 | DIP |
| 8493553 | C 70,000,000.00 |  | 15.00 | . $004 \%$ | . $006 \%$ |  | TTL | 5.00 | 35.00 | DIP |

