

IDENTIFICATION

Product Code: MAINDEC-9A-D01A-D

Product Name: Instruction Test Part I

Date: November 30, 1967

Maintainer: Diagnostic Group

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1. ABSTRACT

All operate group instructions and the following memory reference instructions are tested: LAC, TAD, ADD, SAD, and XOR. Individual error halts are used to describe the failing tests.

2. REQUIREMENTS

2.1 Equipment

PDP-9, 7, 4

2.2 Storage

8K core storage minimum.

2.3 Preliminary Programs

Previous run of memory and memory address test.

3. LOADING PROCEDURE

3.1 Method

HRI tape - Starting address 22_8

HS paper tape reader

4. STARTING PROCEDURE

4.1 Control Switch Settings

Data switch settings equal the complement number of complete program passes between bells.

4.2 Starting Address or Addresses

Starting address 13041_g

Restart address 13041_g

4.3 Program and/or Operator Action

(Reference 4.1)

5. OPERATING PROCEDURE

(Reference 4.1)

6. ERRORS

6.1 Error Halts and Description

Reference symbol table for the address of the individual error halt address (EXXX). The OSR instruction is given a quick check via the program pass complete bell.

OPERATE	E1	E226
LAC	E226A	E316
AND	E316A	E469
XOR	E470	E646
TAD	E647	E879
ADD	E880	E977
SAD	E978	E1105

6.2 Error Recovery

Insert a JMP instruction at the halt address for a test loop.
 Press CONTINUE for additional error halts.

7. RESTRICTIONS

None

8. MISCELLANEOUS

8.1 Execution Time

5 msec/pass