

AA11A,B,C

SCOPE CONTROL
MD-11-DZAAB-A

EP-DZAAB-A-DL-A
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FICHE 1 OF 1

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digital
MADE IN U.S.A.

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ଶ୍ରୀକୃଷ୍ଣାମନ୍ଦିର ପାତ୍ରାମାର୍ଗୀର୍ଥା. ୩ : ୫୩୩ : ୩୩୩

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TEST PROCEDURE

3.1

PROCEDURE FOR NOTARIAL ACTS SHALL BE STATED.

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... **THE S.D. SERIES**

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TEST OR PROGRAM AND PRESS TEST. THE PROGRAM WILL HALT. ~~RESTART~~
IT WILL CONTINUE EXECUTE ALL OF THE CODE.

TO RESUME TESTING, PRESS TEST AGAIN. IF IT IS DESIRED TO
LOOP IN THE TEST, THE FAULT RESTART INSTRUCTION WITH A
JMP (TOP).

NO ERROR CONDITIONS ARE CHECKED DURING THESE TESTS.

RESTRICTIONS

None

1. NO SOFTWARE

2. NO DATA TEST

3. TEST - THE TEST IS PERFORMED AFTER EVERY 100 PASSES
WHICH IS APPROXIMATELY EVERY 30 SECONDS

PROGRAM AND ERASE TEST - IT TAKES APPROXIMATELY 2 MINUTES
15 SECONDS TO COMPLETE ONE PASS FOR THIS TEST

All OTHER TESTS - N/A.

PROGRAM DESCRIPTION

COMPARING ROM STATUS REGISTER TEST

TEST DESCRIPTION
TEST 75: THIS TEST CHECKS THE ROM STATUS REGISTER TO MAKE SURE
ALL BITS MAY BE SET, CLEARED AND READ BACK.

TEST 76: INITIALIZED TEST THAT CLEARED THE FOLLOWING ROM BITS:
TEST DISPLAY INTERRUPT ENABLE (6), TEST LIGHTNING INTERRUPT DISABLE (6),
TEST INTERRUPT OVERLAP (5), MODE (4-3), INTENSITY (2),
ROM SET READY (7).

TEST 77: TEST DISPLAY INTERRUPT ENABLE (6) MAY BE SET AND CLEARED

TEST LIGHTNING INTERRUPT DISABLE (6) MAY BE SET AND CLEARED.

TEST MODE CONTROL (4-3) MAY BE SET AND CLEARED.

TEST INTENSITY (2) MAY BE SET AND CLEARED

TEST ROM READY (7) IS CLEARED WHEN ROM IS PROGRAMMED
WITH MODE 01.

TEST ROM READY (7) IS CLEARED WHEN ROM IS PROGRAMMED
WITH MODE 10.

TEST ROM READY (7) IS SET WHEN ROM IS PROGRAMMED
WITH MODE 00.

TEST ROM READY (7) DOES NOT INTERFERE WITH PROGRAMMING
PROCESSOR MODE (4-3) LEVEL 3.

TEST ROM READY (7) DOES NOT INTERFERE WITH PROGRAMMING
PROCESSOR MODE (4-3) LEVEL 2.

A HORIZONTAL LINE IS DISPLAYED ON THE SCOPES IN THE FOLLOWING ORDER:
1. A HORIZONTAL LINE IS DISPLAYED ON THE SCOPES IN THE SAME MANNER AS FOR A HORIZONTAL LINE (SEE 9.2), EXCEPT THAT THE Y VALUE IS INCREASED WHILE HOLDING THE X VALUE AT ZERO.

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9.3 DISPLAY VERTICAL LINE (STORAGE AND NON-STORAGE).

A VERTICAL LINE IS DISPLAYED ON THE SCOPES IN THE SAME MANNER AS FOR A HORIZONTAL LINE (SEE 9.2), EXCEPT THAT THE X VALUE IS INCREASED WHILE HOLDING THE Y VALUE AT ZERO.

9.4 DISPLAY SQUARE (STORAGE AND NON-STORAGE).

A SQUARE IS DISPLAYED BY INITIALLY SETTING THE X AND Y VALUES TO NEGATIVE FULL SCALE, THEN X IS INCREASED TO POSITIVE FULL SCALE (SECTION LINE); THEN Y IS INCREASED TO POSITIVE FULL SCALE (RIGHT LINE); THEN X IS DECREASED TO NEGATIVE FULL SCALE (TOP LINE); AND FINALLY Y IS DECREASED TO NEGATIVE FULL SCALE (LEFT LINE). MODE 5: INTENSITY ON (SEEING X) AND MODE 10 (INTENSITY ON LOGGING Y) ARE USED.

9.5 DISPLAY X (STORAGE AND NON-STORAGE).

ON X IS DISPLAYED BY INITIALLY SETTING THE X AND Y VALUES TO NEGATIVE FULL SCALE AND THEN HOLDING THE X IN TO POSITIVE FULL SCALE (SEE 9.2). ON THE LEFT SECTION LINE, MODE 5 IS USED. TO NEGATIVE FULL SCALE Y BECOMES A POSITIVE FULL SCALE AND THEN X IS INCREASED WHILE Y IS DECREASED FROM POSITIVE FULL SCALE AGAIN. MODE 5 IS USED ON THE TOP SECTION LINE. MODE 10 (INTENSITY ON LOGGING Y) IS USED.

9.6 DISPLAY LOW AND HIGH INTENSITY NON-STORAGE MODE.

THE LOW INTENSITY IS DISPLAYED ON THE LEFT SECTION LINE.

9.7 DISPLAY ALPHANUMERIC CHARACTER SET (STORAGE AND NON-STORAGE).

THE ALPHABET AND NUMBERS IN TABLE 3 ARE DISPLAYED.

9.8 PHOSPHOR AND ERASE TEST (STORAGE ONLY).

EACH SECOND POINT ON THE FACE OF THE SCOPE IS IMPRESSED ON THE PHOSPHOR AND WHEN THE PROGRAM IS RUN, THIS WILL BE SEEN. THE PROGRAM IS USED TO TEST THE PHOSPHOR COATING ON THE SCREEN. ON THE LEFT SECTION LINE, MODE 5 IS USED. ON THE RIGHT SECTION LINE, MODE 10 IS USED. ON THE FOLLOWING TESTS, MODE 5 WILL BE USED:

1. WITH INTENSITY 5: CAUSE READY ?; % CLEAR MODE
2. % ERASE TEST ON THE LEFT SECTION LINE.
3. DOES ERASE ?; CLEAR TEST.

ON THE LEFT SECTION LINE, MODE 5 IS USED. ON THE RIGHT SECTION LINE, MODE 10 IS USED. ON THE PHOSPHOR TEST, MODE 5 IS USED.

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0011-0. 3 SCOPE CORNER TEST
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1965 NOV 11 27-72 23-48-6 5:39 PAGE 2
2000.111

2-100-2 2:3 PAGE 3

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				TEST THAT REG WILL ACCEPT A COUNT PATTERN 3-377
			15:	: INITIALIZE COUNT PATTERN
			16:	,0, 10 REG
				,0, 10 COUNT
				; ERROR, REG NOT = 0
			55:	
			56:	
			57:	REG WILL ACCEPT A COUNT PATTERN .000-777
			58:	: INITIALIZE COUNT PATTERN
			59:	,0, 15 REG
			60:	,0, 15 COUNT
				; ERROR, REG NOT = 0
			7777 175:6 78:	
				TEST THAT REG WILL ACCEPT A COUNT PATTERN (4000-7777)
			79:	11 SHOULD SET REG 12-15
			80:	: INITIALIZE COUNT PATTERN
			81:	,0, 1 TO REG
			82:	,0, 1 TO PATTERN
				; ERROR, REG NOT = 0
			17500 176:6 78:	
				TEST THAT REG WILL ACCEPT A COUNT PATTERN (4000-7777)
			79:	11 SHOULD SET REG 12-15
			80:	: INITIALIZE COUNT PATTERN
			81:	,0, 1 TO REG
			82:	,0, 1 TO PATTERN
				; ERROR, REG NOT = 0
			17702 178:6 78:	
				TEST THAT REG WILL ACCEPT A COUNT PATTERN (4000-7777)
			79:	11 SHOULD SET REG 12-15
			80:	: INITIALIZE COUNT PATTERN
			81:	,0, 1 TO REG
			82:	,0, 1 TO PATTERN
				; ERROR, REG NOT = 0
			17777 179:6 78:	
				TEST THAT REG WILL ACCEPT A COUNT PATTERN (4000-7777)
			79:	11 SHOULD SET REG 12-15
			80:	: INITIALIZE COUNT PATTERN
			81:	,0, 1 TO REG
			82:	,0, 1 TO PATTERN
				; ERROR, REG NOT = 0

J01

23-988-7 37:39 PAGE 4

: TEST THAT INIT CLEARED THE FOLLOWING REGISTERS B:S.
: FLAG (15), DISPLAY INT ON (6), INTR (5), CODE (4),
: INTR (2), MC SET READY (7).

: IS MC SET READY REGISTER 0:0

: MC SET READY IS ON. MC SET READY IS SET.

: TEST THAT DISPLAY INTERRUPT CLEAR. MC SET READY AND MC CLEAR

: MC SET READY PRESSED

: ERROR. B:S 6 MC SET

: ERROR. B:S 5 MC SET AND CLEAR

: MC SET READY PRESSED

: ERROR. B:S 5 MC SET

: MC SET

: MC SET READY PRESSED

: ERROR. B:S 3 MC SET

: MC SET READY PRESSED

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2011. 0001. 0002. 23-100-6 2:39 PAGE 5
2000.00

000000 1713 000200	000000 1713 000200	177206 177136 177135 177132 177131 177130	01:00: 01:00: 01:00: 01:00: 01:00: 01:00:	000000 000000 000000 000000 000000 000000	.000000 0:0:0 .000000 0:0:0 .000000 0:0:0 .000000 0:0:0 .000000 0:0:0 .000000 0:0:0
000000 1713 000200	000000 1713 000200	177136 177135 177132 177131 177130	01:00: 01:00: 01:00: 01:00: 01:00:	000000 000000 000000 000000 000000	.000000 0:0:0 .000000 0:0:0 .000000 0:0:0 .000000 0:0:0 .000000 0:0:0
000000 1713 000200	000000 1713 000200	177135 177132 177131 177130	01:00: 01:00: 01:00: 01:00:	000000 000000 000000 000000	.000000 0:0:0 .000000 0:0:0 .000000 0:0:0 .000000 0:0:0
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000000 1713 000200	000000 1713 000200	177131 177130	01:00: 01:00:	000000 000000	.000000 0:0:0 .000000 0:0:0
000000 1713 000200	000000 1713 000200	177130	01:00:	000000	.000000 0:0:0

2011: 2072: 23-000-7 07:31 PAGE 6

17670	TEST THAT READ IS CORRECT BY 114: 1578 2500 17670	114: 1578 2500	114: 1578 2500 17670	TEST THAT READ IS CORRECT BY 114: 1578 2500 17670
17670	114: 1578 2500	114: 1578 2500	114: 1578 2500	114: 1578 2500
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17670	114: 1578 2500	114: 1578 2500	114: 1578 2500	114: 1578 2500

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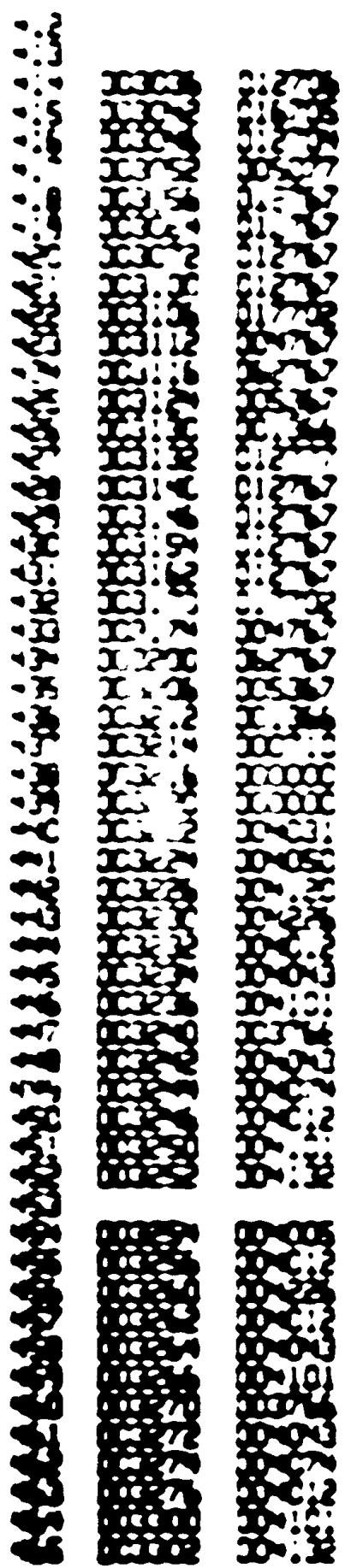
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0:05: 0:06: 0:07: 0:08:
0:09: 0:10: 0:11: 0:12:

0:13: 0:14: 0:15: 0:16:
0:17: 0:18: 0:19: 0:20:

E02

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REC'D: 20.7.22 23-100-6 27:30 PAGE 14

602

321 A: .00E 17.21.21.21.17
111 B: .00E 17.111.111.111.16
101 C: .00E 7.101.101.101.7
121 D: .00E 17.101.101.101.7
111 E: .00E 17.111.111.111.101
011 F: .00E 17.11.11.11.1
121 G: .00E 7.101.121.121.12
C10 H: .00E 17.10.10.10.17
17 I: .00E 9.101.17.101.9
130 J: .00E 60.100.100.100.77
07 K: .00E 17.10.7.7.101
100 L: .00E 17.120.100.100.100
C10 M: .00E 17.9.10.9.17
010 N: .00E 17.9.10.9.17
101 O: .00E 7.101.101.101.7
311 P: .00E 17.1.11
121 Q: .00E 7.101.101.101.17
311 R: .00E 17.11.11.11.16
111 S: .00E 7.111.111.111.12
17 T: .00E 1.1.17.1.1
100 U: .00E 7.100.100.100.7
100 V: .00E 7.7.100.100.7
C10 W: .00E 17.20.10.20.17
C10 X: .00E 10.20.10.20.10
170 Y: .00E 3.9.170.9.3
111 Z: .00E 101.121.111.105.13
17 Z: .00E 8.122.17.100.8
111 AA: .00E 102.121.111.105.13

101 10: .NET 4.1.1,111,111,111
102 11: .NET 3.2.2,17,20
103 12: .NET 47,105,105,105,71
104 13: .NET 7,111,111,111,111
105 14: .NET 101,91,21,11,7
106 15: .NET 46,111,111,111,111
107 16: .NET 46,111,111,111,111
108 17: .NET 7,12,111,111,7
109 space: .NET 0,0,0,0,0

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