

NS486SXF EVALUATION BOARD
870010936-400

Revised: November 13, 1996
Revision: D
November 13, 1996 10:07:29 Page 1

Identical References

Total Duplicate Pairs Encountered : 0

Item	Part	Reference	SheetName	Sheet	Filename
1	1.84MHZ	Input		2	EVB2.SCH
2	1.84MHZ	Output		3	EVB3.SCH
3	14.318MHZ	Output		3	EVB3.SCH
4	14.318MHZ	Input		5	EVB5.SCH
5	A[0..23]	Output	<<<root>>>	1	EVB.SCH
6	A[0..23]	Input		2	EVB2.SCH
7	A[24..25]	Output	<<<root>>>	1	EVB.SCH
8	A[24..25]	Input		4	EVB4.SCH
9	CLK_2X	Input	<<<root>>>	1	EVB.SCH
10	CLK_2X	Input		5	EVB5.SCH
11	CLK_2X	Output		3	EVB3.SCH
12	CLK_FS2	Output		3	EVB3.SCH
13	CLK_FS2	Input		5	EVB5.SCH
14	C_ACK/	Input	<<<root>>>	1	EVB.SCH
15	C_ACK/	Output		3	EVB3.SCH
16	C_AFD/	Input	<<<root>>>	1	EVB.SCH
17	C_AFD/	Output		3	EVB3.SCH
18	C_BUSY	Input	<<<root>>>	1	EVB.SCH
19	C_BUSY	Output		3	EVB3.SCH
20	C_BVD1	Input	<<<root>>>	1	EVB.SCH
21	C_BVD1	Output		4	EVB4.SCH
22	C_BVD2	Input	<<<root>>>	1	EVB.SCH
23	C_BVD2	Output		4	EVB4.SCH
24	C_CD1/	Input	<<<root>>>	1	EVB.SCH
25	C_CD1/	Output		4	EVB4.SCH
26	C_CD2/	Input	<<<root>>>	1	EVB.SCH
27	C_CD2/	Output		4	EVB4.SCH
28	C_CD_RST	Output	<<<root>>>	1	EVB.SCH
29	C_CD_RST	Input		4	EVB4.SCH
30	C_CS0/	Output	<<<root>>>	1	EVB.SCH
31	C_CS0/	Input		5	EVB5.SCH
32	C_CS1/	Output	<<<root>>>	1	EVB.SCH
33	C_CS1/	Input		4	EVB4.SCH
34	C_CS2/	Output	<<<root>>>	1	EVB.SCH
35	C_CS2/	Input		4	EVB4.SCH
36	C_CS3/	Input		2	EVB2.SCH
37	C_CS3/	Input		5	EVB5.SCH
38	C_CS3/	Output	<<<root>>>	1	EVB.SCH
39	C_CS4/	Output	<<<root>>>	1	EVB.SCH
40	C_CS4/	Input		5	EVB5.SCH
41	C_CS5/	Output	<<<root>>>	1	EVB.SCH
42	C_CS5/	Input		5	EVB5.SCH
43	C_CS16/	Input	<<<root>>>	1	EVB.SCH
44	C_CS16/	Input		4	EVB4.SCH
45	C_CS16/	Output		5	EVB5.SCH
46	C_DACK0/	Output	<<<root>>>	1	EVB.SCH
47	C_DACK0/	Input		5	EVB5.SCH
48	C_DACK2/	Output	<<<root>>>	1	EVB.SCH
49	C_DACK2/	Input		5	EVB5.SCH
50	C_DACK4/	Output	<<<root>>>	1	EVB.SCH
51	C_DACK4/	Input		5	EVB5.SCH
52	C_DACK6/	Output	<<<root>>>	1	EVB.SCH
53	C_DACK6/	Input		5	EVB5.SCH

Item	Part	Reference	SheetName	Sheet	Filename
54	C_DIR	Output	<<<root>>>	1	EVB.SCH
55	C_DIR	Input		4	EVB4.SCH
56	C_DRQ0	Input	<<<root>>>	1	EVB.SCH
57	C_DRQ0	Output		5	EVB5.SCH
58	C_DRQ2	Input	<<<root>>>	1	EVB.SCH
59	C_DRQ2	Output		5	EVB5.SCH
60	C_DRQ4	Input	<<<root>>>	1	EVB.SCH
61	C_DRQ4	Output		5	EVB5.SCH
62	C_DRQ6	Input	<<<root>>>	1	EVB.SCH
63	C_DRQ6	Output		5	EVB5.SCH
64	C_DSR/	Input	<<<root>>>	1	EVB.SCH
65	C_DSR/	Output		2	EVB2.SCH
66	C_DTR/	Output	<<<root>>>	1	EVB.SCH
67	C_DTR/	Input		2	EVB2.SCH
68	C_ENABLE/	Output	<<<root>>>	1	EVB.SCH
69	C_ENABLE/	Input		4	EVB4.SCH
70	C_ERR/	Input	<<<root>>>	1	EVB.SCH
71	C_ERR/	Output		3	EVB3.SCH
72	C_INIT/	Bidirectional	<<<root>>>	1	EVB.SCH
73	C_INIT/	Bidirectional		3	EVB3.SCH
74	C_IOIS16/	Input	<<<root>>>	1	EVB.SCH
75	C_IOIS16/	Output		4	EVB4.SCH
76	C_IORD/	Input		4	EVB4.SCH
77	C_IORD/	Input		2	EVB2.SCH
78	C_IORD/	Input		5	EVB5.SCH
79	C_IORD/	Output	<<<root>>>	1	EVB.SCH
80	C_IOWR/	Input		4	EVB4.SCH
81	C_IOWR/	Input		2	EVB2.SCH
82	C_IOWR/	Input		5	EVB5.SCH
83	C_IOWR/	Output	<<<root>>>	1	EVB.SCH
84	C_IRQ0	Input	<<<root>>>	1	EVB.SCH
85	C_IRQ0	Output		5	EVB5.SCH
86	C_IRQ1	Input	<<<root>>>	1	EVB.SCH
87	C_IRQ1	Output		5	EVB5.SCH
88	C_IRQ2	Input	<<<root>>>	1	EVB.SCH
89	C_IRQ2	Output		2	EVB2.SCH
90	C_IRQ2	Output		5	EVB5.SCH
91	C_IRQ3	Input	<<<root>>>	1	EVB.SCH
92	C_IRQ3	Output		5	EVB5.SCH
93	C_IRQ4	Input	<<<root>>>	1	EVB.SCH
94	C_IRQ4	Output		5	EVB5.SCH
95	C_IRQ5	Input	<<<root>>>	1	EVB.SCH
96	C_IRQ5	Output		4	EVB4.SCH
97	C_IRQ5	Output		5	EVB5.SCH
98	C_MEMR/	Input		4	EVB4.SCH
99	C_MEMR/	Input		3	EVB3.SCH
100	C_MEMR/	Input		5	EVB5.SCH
101	C_MEMR/	Output	<<<root>>>	1	EVB.SCH
102	C_MEMW/	Input		4	EVB4.SCH
103	C_MEMW/	Input		3	EVB3.SCH
104	C_MEMW/	Input		5	EVB5.SCH
105	C_MEMW/	Output	<<<root>>>	1	EVB.SCH
106	C_NMI	Input	<<<root>>>	1	EVB.SCH

Item	Part	Reference	SheetName	Sheet	Filename
107	C_NMI	Output		5	EVB5.SCH
108	C_PD[0..7]	Bidirectional <<<root>>>		1	EVB.SCH
109	C_PD[0..7]	Bidirectional		3	EVB3.SCH
110	C_PE	Input <<<root>>>		1	EVB.SCH
111	C_PE	Output		3	EVB3.SCH
112	C_RDY	Input <<<root>>>		1	EVB.SCH
113	C_RDY	Output		5	EVB5.SCH
114	C_REG/	Output <<<root>>>		1	EVB.SCH
115	C_REG/	Input		4	EVB4.SCH
116	C_RESET	Input		2	EVB2.SCH
117	C_RESET	Input		5	EVB5.SCH
118	C_RESET	Output <<<root>>>		1	EVB.SCH
119	C_RESET/	Output <<<root>>>		1	EVB.SCH
120	C_RESET/	Input		3	EVB3.SCH
121	C_RTS/	Output <<<root>>>		1	EVB.SCH
122	C_RTS/	Input		2	EVB2.SCH
123	C_RXD/	Input <<<root>>>		1	EVB.SCH
124	C_RXD/	Output		2	EVB2.SCH
125	C_SBHE/	Input <<<root>>>		1	EVB.SCH
126	C_SBHE/	Input		5	EVB5.SCH
127	C_SLCT	Input <<<root>>>		1	EVB.SCH
128	C_SLCT	Output		3	EVB3.SCH
129	C_SLIN/	Bidirectional <<<root>>>		1	EVB.SCH
130	C_SLIN/	Bidirectional		3	EVB3.SCH
131	C_STB/	Bidirectional <<<root>>>		1	EVB.SCH
132	C_STB/	Bidirectional		3	EVB3.SCH
133	C_TC	Output <<<root>>>		1	EVB.SCH
134	C_TC	Input		5	EVB5.SCH
135	C_TXD/	Output <<<root>>>		1	EVB.SCH
136	C_TXD/	Input		2	EVB2.SCH
137	C_VCC_SEL/	Output <<<root>>>		1	EVB.SCH
138	C_VCC_SEL/	Input		4	EVB4.SCH
139	C_VPP_SEL1	Output <<<root>>>		1	EVB.SCH
140	C_VPP_SEL1	Input		4	EVB4.SCH
141	C_VPP_SEL2	Output <<<root>>>		1	EVB.SCH
142	C_VPP_SEL2	Input		4	EVB4.SCH
143	D[0..7]	Bidirectional <<<root>>>		1	EVB.SCH
144	D[0..7]	Bidirectional		2	EVB2.SCH
145	D[0..15]	Bidirectional <<<root>>>		1	EVB.SCH
146	D[0..15]	Bidirectional		3	EVB3.SCH
147	D[0..15]	Bidirectional		4	EVB4.SCH
148	D[0..15]	Bidirectional		5	EVB5.SCH
149	FLASH/	Input		3	EVB3.SCH
150	FLASH/	Output		5	EVB5.SCH
151	FVPP	Output <<<root>>>		1	EVB.SCH
152	FVPP	Input		3	EVB3.SCH
153	PCMRDY	Output		4	EVB4.SCH
154	PCMRDY	Input		5	EVB5.SCH
155	P_ENA1/	Output		4	EVB4.SCH
156	P_ENA1/	Input		5	EVB5.SCH
157	P_ENA2/	Output		4	EVB4.SCH
158	P_ENA2/	Input		5	EVB5.SCH
159	SA[0..23]	Input		4	EVB4.SCH

Item	Part	Reference	SheetName	Sheet	Filename
160	SA[0..23]	Input		5	EVB5.SCH
161	SA[0..23]	Output		2	EVB2.SCH
162	SA[1..19]	Output		2	EVB2.SCH
163	SA[1..19]	Input		3	EVB3.SCH
164	U_OUT1/	Input	<<<root>>>	1	EVB.SCH
165	U_OUT1/	Output		2	EVB2.SCH
166	U_OUT2/	Input	<<<root>>>	1	EVB.SCH
167	U_OUT2/	Output		2	EVB2.SCH

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Revision: D

Cross Reference: Module Ports November 13, 1996 10:07:29 Page 6

Type Mismatch

Total Mismatches Encountered : 0