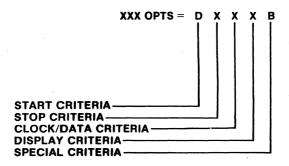
#### **FAULT DETECTION OPTIONS**

The operator selects the measurement by depressing the appropriate key and then the OPTSEL key. The following will be displayed:



The operator specifies the criteria by digits which have preassigned meanings. The digits and their meanings are described in the following sections.

# **START DIGIT CONDITIONS**

START DIGIT	CONDITION	SIGNATURE	FREQUENCY	COUNT	INTERVAL	
0	Using external start probe negative going edge	X	х	Х	х	
1	Using external start probe, positive going edge	х	х	Χ	х	
. 2	Using external data probe, negative going edge	Х	х	х	х	
3	Using external data probe, positive going edge	Х	Х	х	х	
4	Address Comparison	Χ	Χ	Х	Χ	
5	Address Comparison and external start probe, negative going edge	Х	х	Х	х	
6	Address Comparison and external start probe, positive going edge	х	х	х	х	
7	Address Comparison and external data probe negative going edge	Х	Х	х	х	
8	Address Comparison and external data probe, positive going edge	Х	Х	х	х	
9	Start immediately	X	Χ			

# STOP DIGIT CONDITIONS

STOP DIGIT	CONDITION	SIGNATURE	FREQUENCY COUNT INTERVAL
0	Using external stop probe negative going edge	Х	хх
1	Using external stop probe positive going edge	Х	хх
2	Using external data probe negative going edge	х	хх
3	Using external data probe, positive going edge	х	хх
4	Address Comparison	Χ	хх
5	Address comparison and external stop probe, negative going edge	х	хх
6	Address comparison and external stop probe, positive going edge	х	хх
7	Address comparison and external data probe negative going edge	х	хх
8	Address comparison and external data probe positive going edge	x	хх
9	Count	Χ	X
Α	Count and external stop probe, negative going edge	X	
В	Count and external stop probe, positive going edge	х	
С	Count and address comparison	Χ	
Ď	Count and address comparison and external stop probe, negative going edge	x	
E	Count and address comparison and external stop probe, positive going edge	x	

## **CLOCK/DATA CONDITIONS**

	• • • • • • • • • • • • • • • • • • • •							
CLOCK/DATA DIGIT	CLOCK SOURCE	START/STOP SYNC	DATA SOURCE	SIGNATURE	SIGNATURE	FREQUENCY	COUNT	INTAL
0	Clock Probe	+	Data Probe	+	Χ			
1	Clock Probe	+	Data Probe	-	Х			
2	Clock Probe	-	Data Probe	+	Х			
3	Clock Probe	-	Data Probe	-	Χ			
. 4	OPREQ	+	Data Probe	+	Х			
5	OPREQ	+	Data Probe	-	Х			
6	OPREQ	-	Data Probe	+	Х			
7	OPREQ	-	Data Probe	-	Х			
8			Data Probe			Χ	P	Χ
9			OPREQ				Ρ	
Α			Data Probe (Transitions)				Т	

P = Pulse Count

T = Transition Count

### **DISPLAY CONDITIONS**

Display Digits	Meaning	SIGNATURE FREQUENCY COUNT INTERVAL
0	Average of 10 samples	XXX
1	No Average. No wait.	$x \times x \times x$
2	Wait 100 msec. between displays	$x \times x \times x$
3	Wait 200 msec. between displays	X X X X
4	Wait 300 msec. between displays	$x \times x \times x$
5	Wait 400 msec. between displays	$\mathbf{x} \mathbf{x} \mathbf{x} \mathbf{x}$
6	Wait 500 msec. between displays	$\mathbf{x} \mathbf{x} \mathbf{x} \mathbf{x}$
7	Wait 700 msec. between displays	$\mathbf{x} \mathbf{\dot{x}} \mathbf{x} \mathbf{x}$
8	Wait 1 second between displays	$\times \times \times \times$
9	Wait 2 seconds between displays	X X X X
· A	Wait 5 seconds between displays	X X X X

#### SPECIAL CRITERIA

The special criteria digit indicates how memory is to be mapped. If it is zero, all memory except the test diagnostic memory is mapped to the SUT. If it is a one, all memory is mapped to the  $\mu$ SA; however, all control, address and data signals are passed to the SUT. While mapped to the  $\mu$ SA, any memory control and data signals from the SUT are disregarded. This allows fault analysis on the SUT memory and BUS structure.

### **BREAK POINT OPTIONS**

BITS	FUNCTION
0	$0 = Pause \mu P$ , $1 = Jump to Address$
1	1 = Pass Count Enable
2	1 = Break On Read Access
3	1 = Break On Write Access

## **FILTER OPTIONS**

FILTER DIGIT	FUNCTION
0	No Filter
1	50 nsec
2	100 nsec
3	150 nsec