



GenRad

**RS-232 MULTIPORT INTERFACE
(PM-DLV11J and DEC DLV11-J)
INSTALLATION PROCEDURE**

**Form No. 2511-1001
Revision 2
January 1984**

L PURPOSE

The RS-232 Multiport Interface (2511-9401) is a prerequisite for the DataLink product (2511-9409) and the TEK 4662 Digital Plotter (2511-9405). This procedure outlines the steps necessary to install and test the Plessey PM-DLV11J and DEC DLV11-J interfaces.

The interfaces and cabling to the rear panel support 4 RS-232 ports. Port 2 is reserved for DataLink and Port 3 is reserved for the digital plotter. Ports 1 and 4 are currently unassigned. They may be used as alternate ports for DataLink or for supporting an auxiliary terminal.

The base register address and interrupt vector are 775640 and 340, respectively; therefore, the 4 ports are allocated as follows:

	<u>First Register</u>	<u>Interrupt Vector</u>	<u>Baud Rate</u>	<u>Reserved For</u>
Port 1	775640	340	9600	-
Port 2	775650	350	1200	DataLink
Port 3	775660	360	1200	Dig. Plotter
Port 4	775670	370	9600	-

II. INSTALLATION OF PLESSEY PM-DLV11J

See Figure 1 for switch and jumper placement.

The required switch settings are as follows:

<u>Switch-Position</u>	<u>Setting</u>	<u>Switch-Position</u>	<u>Setting</u>
S1-1	OFF	S2-1	OFF
2	OFF	2	ON
3	ON	3	OFF
4	OFF	4	OFF
5	OFF	5	OFF
6	OFF	6	ON
7	OFF	7	OFF
8	ON	8	OFF
		9	OFF
		10	OFF

The baud rates are jumper seletable on wire wrap pins CLK A, 4, 3, 2, 1, B, C, D, E, F, G, H, and J. CLK 1-4 represent ports 1-4

The following wire wrap connections are required:

1 to F	(9600)
2 to A	(1200)
3 to A	(1200)
4 to F	(9600)

The following table defines the available baud rates:

<u>Baud Rate</u>	<u>Wire-Wrap Pin</u>
150	B
300	G
600	C
1200	A
2400	D
4800	E
9600	F
19.2K	J
38.4K	H

The following table defines STOP BIT, DATA, and PARITY SELECT:

<u>Remove</u>	<u>Install</u>
B3 - A3	B4 - A4
B2 - A2	
B5 - A5	
B1 - A1	

* UART for channel 1 is U19; channel 2 is U17; channel 3 is U15; channel 4 is U13.

The following table defines RS-232 interface jumpers:

	<u>From</u>	<u>To</u>
Channel 1	1E	1F
	1D	1B
Channel 2	2E	2F
	2D	2B
Channel 3	3E	3F
	3D	3B
Channel 4	4E	4F
	4D	4B

III. INSTALLATION OF DEC DLV11-J

See Figure 2 for the following jumper placements:

	<u>Wrap Pin</u>	<u>From</u>	<u>To</u>
	M0	X	3
	N0	X	3
	M1	X	3
	N1	X	3
CH0	E	0	X
	D	1	X
	S	0	X
	P	1	X
CH1	E	0	X
	D	1	X
	S	0	X
	P	1	X
CH2	E	0	X
	D	1	X
	S	0	X
	P	1	X
CH3	E	0	X
	D	1	X
	S	0	X
	P	1	X
	M2	3	X
	N2	3	X
	M3	3	X
	N3	3	X
	A5	1	X
	A9	1	X
	A12	1	X
	A10	0	X
	A11	1	X
	A8	1	X
	C2	0	X
	C1	0	X
	V5	1	X
	A6	No connection	
	A7	Shorted	
	V6	Shorted	
	V7	Shorted	
	M	Shorted	
	B-X-H	No connection	
	L-U-T	No connection	
	0	0	N
	1	1	W
	2	2	W
	3	3	N
	Z	No connection	
	Y	No connection	
	K	No connection	
	V	No connection	

IV. PROCEDURE

1. Remove the LSI 11 processor from its current Q-BUS backplane slot and insert it in the adjacent slot (toward rear). Insert the PM-DLV11J or DEC-DLV11-J into the vacated slot.
2. Install cable assembly 2511-2030 between the interface and the rear panel. Connector P1 plugs into the interface connector nearest the rear of the system. Connectors P2, P3 and P4 are next in line.
3. Remove the rear plate which covers the RS-232 ports. For the PM-DLV11J, connectors J1, J2, J3, and J4 are fastened to rear panel ports 1, 2, 3 and 4 respectively, using the jack socket assemblies supplied. For the DEC DLV11-J, connectors J1, J2, J3 and J4 are fastened to rear panel ports 4, 3, 2 and 1 respectively.
4. Run diagnostic DLV11 or DLVTST from diskette 2511-0691. Instructions are in manual 2511-0190.

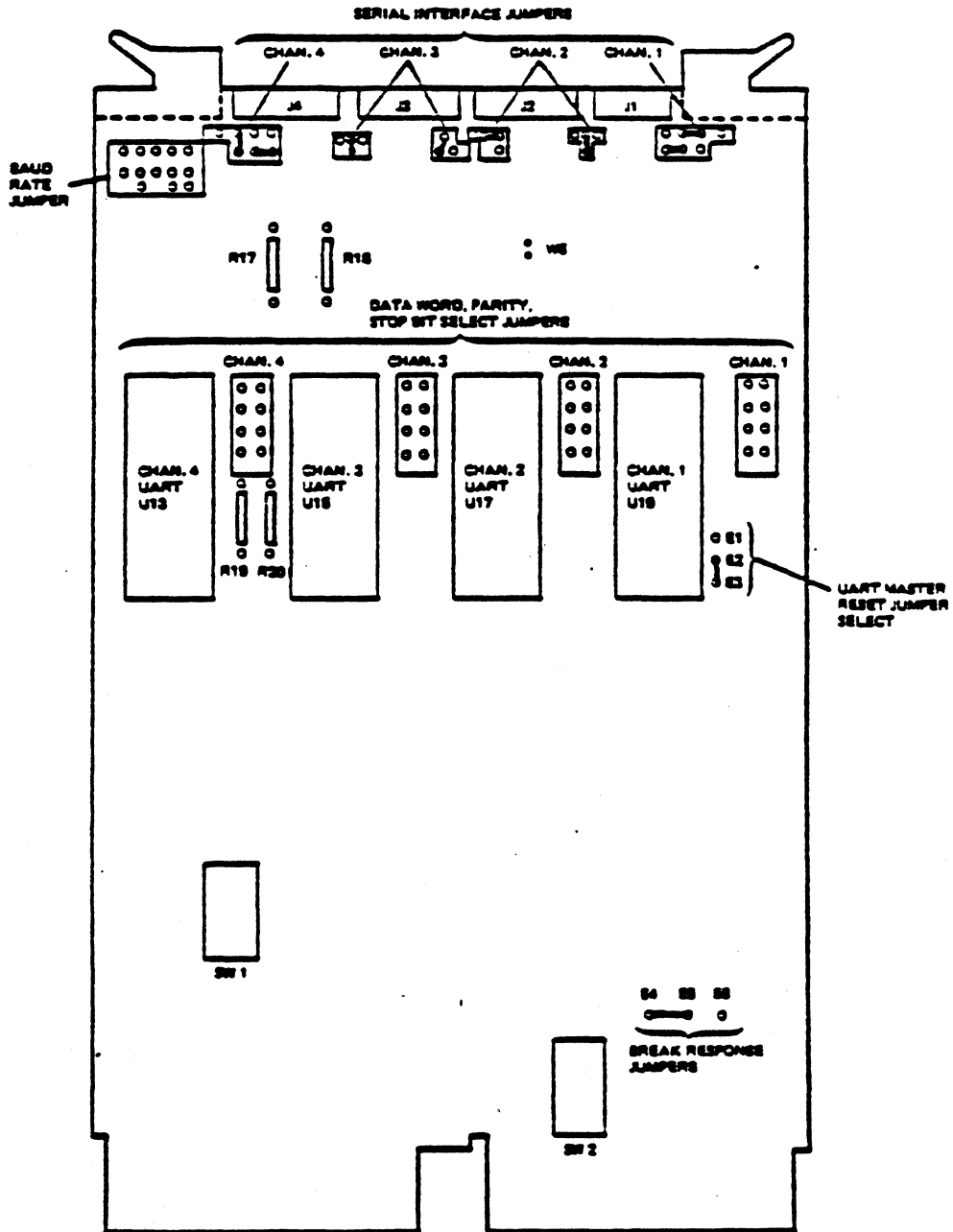


Figure 1. Switch and Jumper Locations

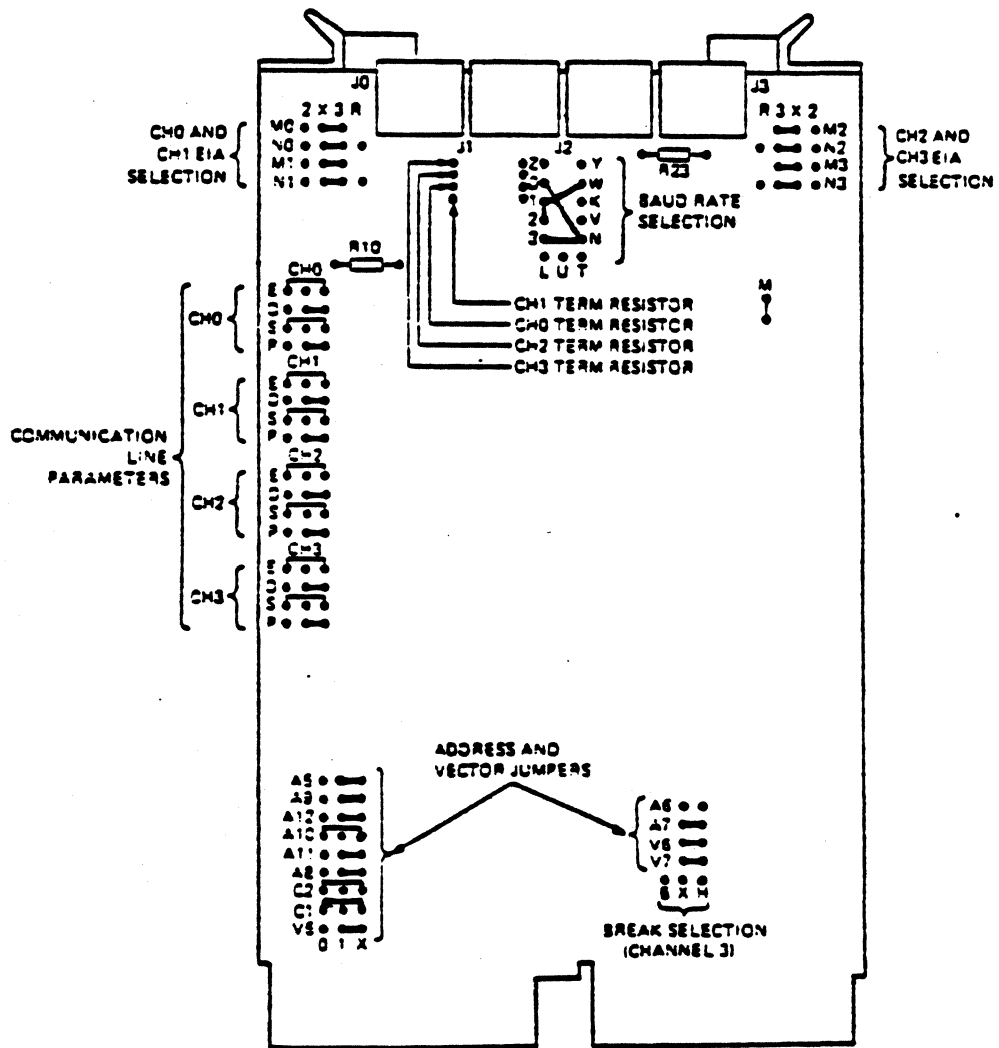


Figure 2 DLV11-J Component and Jumper Factory Configuration Summary