

INTERIM UPDATE
NOTICE
9200/9200 II
9300/9300 II

RELEASE MEMO #147 UPDATE #1

DATE JUNE, 1975

PRODUCT: 9200/9200II/9300/9300II
8411/8414 DNCOS REVISION 5.0.1

DISTRIBUTION

To Lists SA, RS, UA, UB, 174, 175, 176, 177, 178, 179, 184, 185, 186
187, 188, 189, 18A, 17A - Memo & Attachments

PHILADELPHIA DEVELOPMENT CENTER
SOFTWARE DEVELOPMENT SUPPORT
BLUE BELL, PA

SPERRY  **UNIVAC**
COMPUTER SYSTEMS

A. SOFTWARE UNIT:

Rev.

DNCOS 8411/8414 Software

5.0.1

B. REASON FOR RELEASE:

1. To notify users of a problem in the Rev. 5.0 R11T RPG -
See Appendix A.
2. To apply corrections to the Rev. 5.0 release memo -
See Appendix B.

APPENDIX A:

<u>A. SOFTWARE UNIT:</u>	<u>Card Count</u>
R11T - Transient	123
DNCOS 8411/8414 RPG	

B. REASON FOR RELEASE:

To notify users of a problem in the Rev. 5.0 R11T RPG.

C. PROBLEM:

A processor abnormal condition may occur while compiling Rev. 5.0 RPG programs using ISAM chaining files.

D. SOLUTION:

1. Replace the RPG relocatable element NUTC, found on System Preparation Tape #2, with the relocatable card deck supplied with this release.
2. Relink R11T using the linker deck located on System Preparation Tape #1 in source element LTDS. Before relinking, change card number R11T2620 in the linker deck to read as follows:

```
END1 EQU *+X'10F6'+L
```

3. Delete the loadable R11T located on System Preparation Tape #1 (loadable elements R11T through TP1) and replace it with the newly created R11T.

APPENDIX B.

A. AREAS OF CORRECTION TO THE REV. 5.0 RELEASE MEMO:

1. Cover Letter

a. Special Notes - page 2, paragraph 7:

The base address of the loadable Disc Report Program Generator should be X'1380', not X'1080'.

2. Appendix A

a. The following version numbers should be changed on Tape #1:

SUPR V32 should be V34

JBCN V2A should be V2B

PHSO V10 should be V11

PHSA V8 should be VC

PHS2 V13 should be V14

FAST?LDR V3 should be V4

b. Tape #2:

DTFUQ VF should be V10

3. Appendix C

Page 2, paragraph 3:

SYSl - Supervisor size <X'10B0' should read X'1080'.