

Hewlett-Packard HP 9000 Multiuser Systems

Product Enhancement

Hewlett-Packard has significantly enhanced the HP 9000 Series 800 product line with the addition of five new systems plus price reductions on earlier models.

New entries are the low-end Model 832S, mid-range Models 845S and 845SE, and new high-end Models 870S/100 and 870S/200. The new systems are RISC-based, UNIX systems which are object-code compatible with the entire Series 800 family. The new high-end models provide more than twice the performance of the Model 855S.

Hewlett-Packard reduced prices on the Series 800 Model 835S by 18 percent, and on Model 835SE by 21 percent.

Product Definition

Hewlett-Packard now has the largest offering of UNIX computers in the industry. The current HP 9000 Series 800 line comprises Models 808S, 815S, 825S, 832S, 835S, 835SE, 845S, 845SE, 850S, 855S, 870S/100, and 870S/200.

The Model 832S enhances the entry level of the Series 800 family. It is more powerful than Model 815S and Model 825S, offering 15 MIPS, 16M

bytes of main memory, and support for up to 64 users.

Models 845S and 845SE similarly extend the midrange of the Series 800. Both models offer performance of 23 MIPS. Board upgrades are available from Models 835S and 835SE to the Model 845S or 845SE.

At the high end, HP expanded performance with the use of new chip technology in Models 870S/100 and 870S/200. These models offer up to four times the performance of the previous high-end model and six times the main memory. A board swap upgrade is available from the Model 850S or 855S to the new models.

A comparison of HP 9000 Series 800 Models 832S, 845S, 845SE, 870S/100, and 870S/200 appears in Table 1.

Analysis

Hewlett-Packard has nearly doubled the Series 800 product family with these latest introductions. Customers now have the option of a more powerful entry-level system with Model 832S, and the growth capability has been enhanced with Models 845S and 845SE. By filling gaps in the price/performance range, Hewlett-Packard has significantly improved

Table 1. System Comparison

Model	832S	845S/845SE	870S/100, 870S/200
System Characteristics			
Date of introduction	January 1990	January 1990	January 1990
Date of first delivery	March 1990	1Q90	4Q90
Operating System	HP-UX	HP-UX	HP-UX
Upgradable from	Not applicable	825S, 835S, 835SE	850S,855S; 870/100
Upgradable to	Not applicable	Not applicable	870/200; Not applicable
MIPS	15	23	50/95
Relative Performance (based on a rating of the 825S at 1.0)	—	—	—
Memory			
Minimum capacity, bytes	16M	16M/32M	96M/128M
Maximum capacity, bytes	64M	128M	768M
Cache Memory, bytes	128K	256K	1,024K
Input/Output Control			
Number of channels	—	2 to 8	2 to 12
Maximum Disk Storage, bytes			
	8G	21.4G	85.76G
Number of Workstations			
	64	56/100	256/304
Communications Protocols			
	IEEE 802 Ethernet, NS/9000 (NFT), TCP/IP, ARPA/Berkeley, NFS, uucp, SNA/3770, SNA/3270, NS/DEC VAX/VMS	IEEE 802 Ethernet, NS/9000 (NFT), TCP/IP, ARPA/Berkeley, NFS, uucp, SNA/3770, SNA/3270, NS/DEC VAX/VMS	IEEE 802 Ethernet, NS/9000 (NFT), TCP/IP, ARPA/Berkeley, NFS, uucp, SNA/3770, SNA/3270, NS/DEC VAX/VMS
Purchase Price (\$) (basic)			
	51,200	59,500 for 845S; 130,000 for 845SE	419,000 for 870/100; 689,000 for 870/200

the upgrade options within the Series 800 family and has extended the life expectancies of its midrange models.

The new high-end Models 870S/100 and 870S/200 offer mainframe performance levels thanks to HP's new CMOS chip technology. Performance levels up to 95 MIPS are available, providing customers with extensive growth capabilities in this superminicomputer family.

System Features

The HP 9000 Series 800 Models achieve their performance through a design that features:

- HP Precision Architecture.
- NMOS CPU chip (CMOS CPU on Models 870S/100 and 870S/200).
- High-speed memory cache for CPU data and instructions.
- A floating-point co-processor.

System Configurations

The Series 800 Model 832S comes with an SPU containing the following:

- One central processor with 128K bytes of cache memory and a floating-point co-processor.
- 16M bytes of main storage.
- High-capacity backup with 1.3G-byte digital audio tape.
- One asynchronous six-channel multiplexer.
- One HP-IB interface.

The Series 800 Models 845S and 845SE configure as follows:

- One central processor with 256K bytes of cache memory and a floating-point co-processor.
- 16M bytes (32M bytes on Model 845SE) of main storage.
- Battery backup system (optional on Model 845S).
- One asynchronous six-channel multiplexer.

- One HP-IB interface.

The Series 800 Models 870S/100 and 870S/200 configure as follows:

- CMOS CPU chip (two chips on Model 870/200).
- 96M bytes (128M bytes on Model 870/200) of main storage.
- One HP-IB interface.

Availability

The Models 845S and 845SE are available immediately. Model 832S will be available this month, and

Models 870S/100 and 870S/200 will be available in the fourth quarter of 1990.

Base Configuration Pricing

Model 832S: \$51,200.

Model 845S: \$59,500.

Model 845SE: \$130,000.

Model 870S/100: \$419,000.

Model 870S/200: \$689,000.

Price Reductions

The cost of a Series 800 Model 835S has been reduced 18 percent to \$37,000. The cost of Model 835SE is reduced 21 percent to \$80,000. ■